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1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Σ

Top View

Compliance

Standard

2N4

nde

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

Internal Schematic

Case

SOT23

2N4 = Product Type Marking Code

YM = Date Code Marking

Y or  $\overline{Y}$  = Year (ex: G = 2019) M = Month (ex: 9 = September)



DMG6968U

#### N-CHANNEL ENHANCEMENT MODE MOSFET

### Features

- Low On-Resistance
  - $25m\Omega @ V_{GS} = 4.5V$
  - 29mΩ @ V<sub>GS</sub> = 2.5V
  - 36mΩ @ V<sub>GS</sub> = 1.8V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
  - https://www.diodes.com/guality/product-definitions/

ESD PROTECTED TO 2kV

Ordering Information (Note 4)

<1000ppm antimony compounds.

Part Number

DMG6968U-7

Lead-free

**Marking Information** 

 An Automotive-Compliant Part is Available Under Separate Datasheet (DMG6968UQ)

### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3

D

Top View

S

Packaging

3000/Tape & Reel

G

- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

Date Code Key

Notes:

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	W		G	Н	Ι	J	K	L	М	Ν	0	Р
Year	2009		2019	2020	2021	2022	2023	2024	2025	2026	2027	2028

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## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

3

4

5

Characte	eristic		Symbol	Value	Unit	
Drain-Source Voltage			V <sub>DSS</sub>	20	V	
Gate-Source Voltage			V <sub>GSS</sub>	±12	V	
Continuous Drain Current (Note 5)	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	6.5 5.2	A	
Pulsed Drain Current			I <sub>DM</sub>	30	A	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit					
Power Dissipation (Note 5)	PD	1.3	W					
Thermal Resistance, Junction to Ambient @ $T_A = +25^{\circ}C$	R <sub>θJA</sub>	157	°C/W					
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C					

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

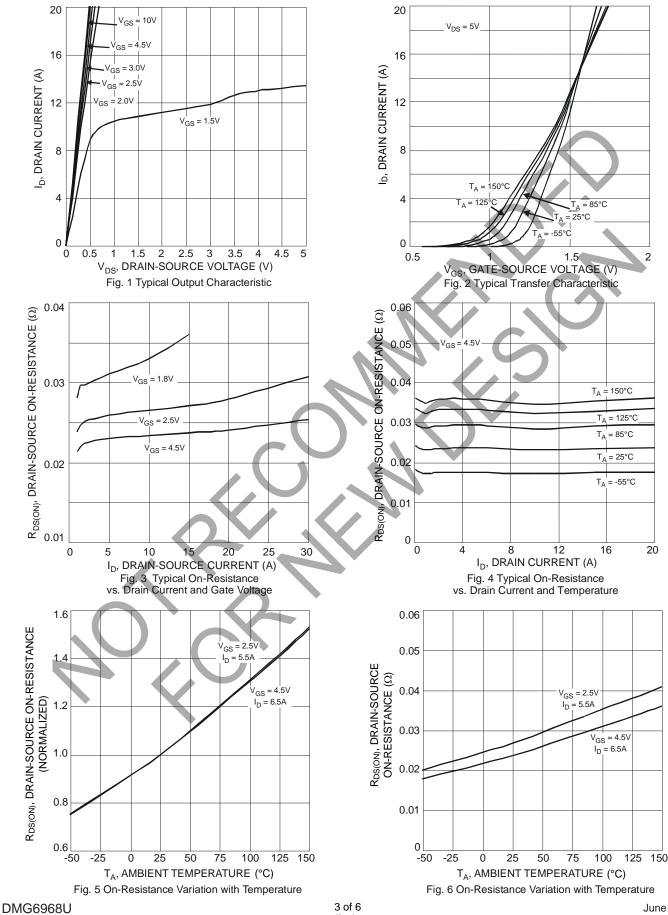
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_		V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>		_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		-	±10	μA	$V_{GS}=\pm 10V,V_{DS}=0V$	
Gate-Source Breakdown Voltage	BVsgs	±12	ł		V	$V_{DS}=0V,I_{G}=\pm250\mu A$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	_	0.9	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
			21	25		$V_{GS} = 4.5V, I_D = 6.5A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		23	29	mΩ	$V_{GS} = 2.5V, I_D = 5.5A$	
			28	36		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 3.5A	
Forward Transfer Admittance	Y <sub>fs</sub>		8	_	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 5A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	151	—	pF		
Output Capacitance		_	91	_	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz	
Reverse Transfer Capacitance		_	32	_	pF		
Total Gate Charge	Qg	_	8.5	_	nC		
Gate-Source Charge		_	1.6	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$	
Gate-Drain Charge	Q <sub>gd</sub>	_	2.8	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	54	_	ns		
Turn-On Rise Time	t <sub>R</sub>		66	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	613	_	ns	$R_L = 10\Omega, R_G = 6\Omega, I_D = 1A$	
Turn-Off Fall Time	tF		205	—	ns		

5. Device mounted on FR-4 substrate PC board, 2oz. copper, with thermal vias to bottom layer 1 inch square copper plate. 6 Short duration pulse test used to minimize self-heating effect. Notes:

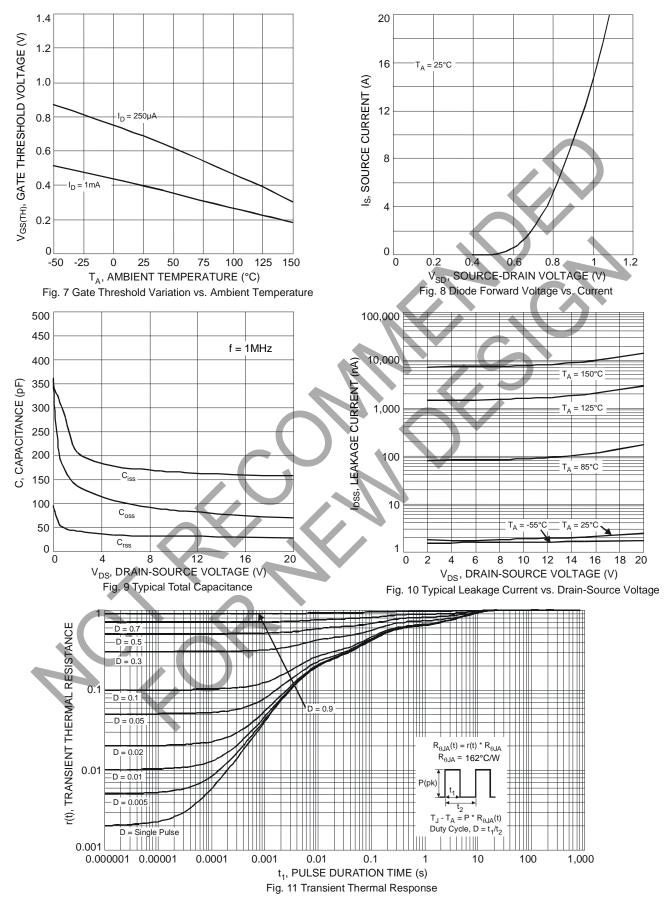
7. Guaranteed by design. Not subject to production testing.



# DMG6968U



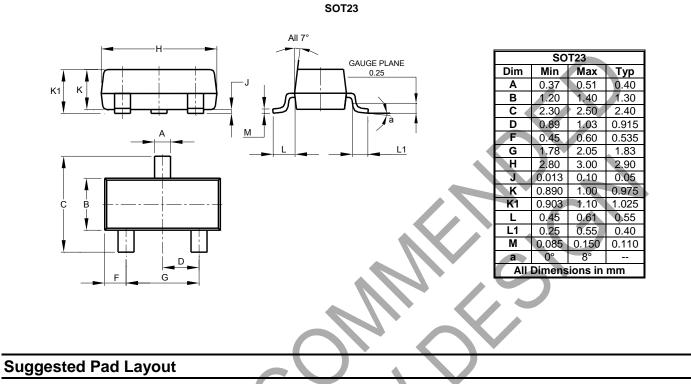




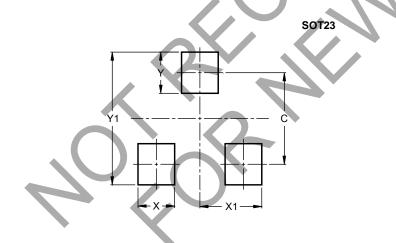


# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



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Dimensions	Value (in mm)					
С	2.0					
Х	0.8					
X1	1.35					
Y	0.9					
Y1	2.9					



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