

■ Dimensions: (mm)

Part No.	A	B	C	D	E
DJNR 5020	4.9 ± 0.2	4.9 ± 0.2	2.0 Max.	1.2 ± 0.2	3.3 ± 0.2

■ Series List

No.	Part No.	L (μ H)	SRF Min. (MHz)	RDC $\pm 20\%$ (Ω)	Isat Max. (mA)	Irms Max. (mA)
1	DJNR5020-1R0-S	1.0	81	0.021	4000	3600
2	DJNR5020-1R5-S	1.5	68	0.026	3350	3200
3	DJNR5020-2R2-S	2.2	57	0.035	2900	2900
4	DJNR5020-3R3-S	3.3	46	0.048	2400	2400
5	DJNR5020-4R7-S	4.7	37	0.060	2000	2000
6	DJNR5020-6R8-S	6.8	30	0.090	1600	1650
7	DJNR5020-100-S	10	24	0.120	1300	1450
8	DJNR5020-150-S	15	20	0.165	1100	1200
9	DJNR5020-220-S	22	17	0.260	900	1000

1. Test Frequency : 100KHz

2. Tolerance : N \pm 30% ; M \pm 20%

3. Isat : The value of current causes a 30% inductance reduction from initial value.

4. Irms : The value of current causes a 40°C temperature rise.

5. Rated Current: Either Isat or Irms whichever is smaller.

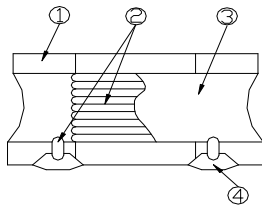
6. Operating Temperature Range : -25°C to +125°C (Including self-temperature rise)

7. Storage Temp. Range: -40°C to +85°C

□ PACKAGE

Type	DJNR 5020
Q'TY/Reel	800

Structural Drawing

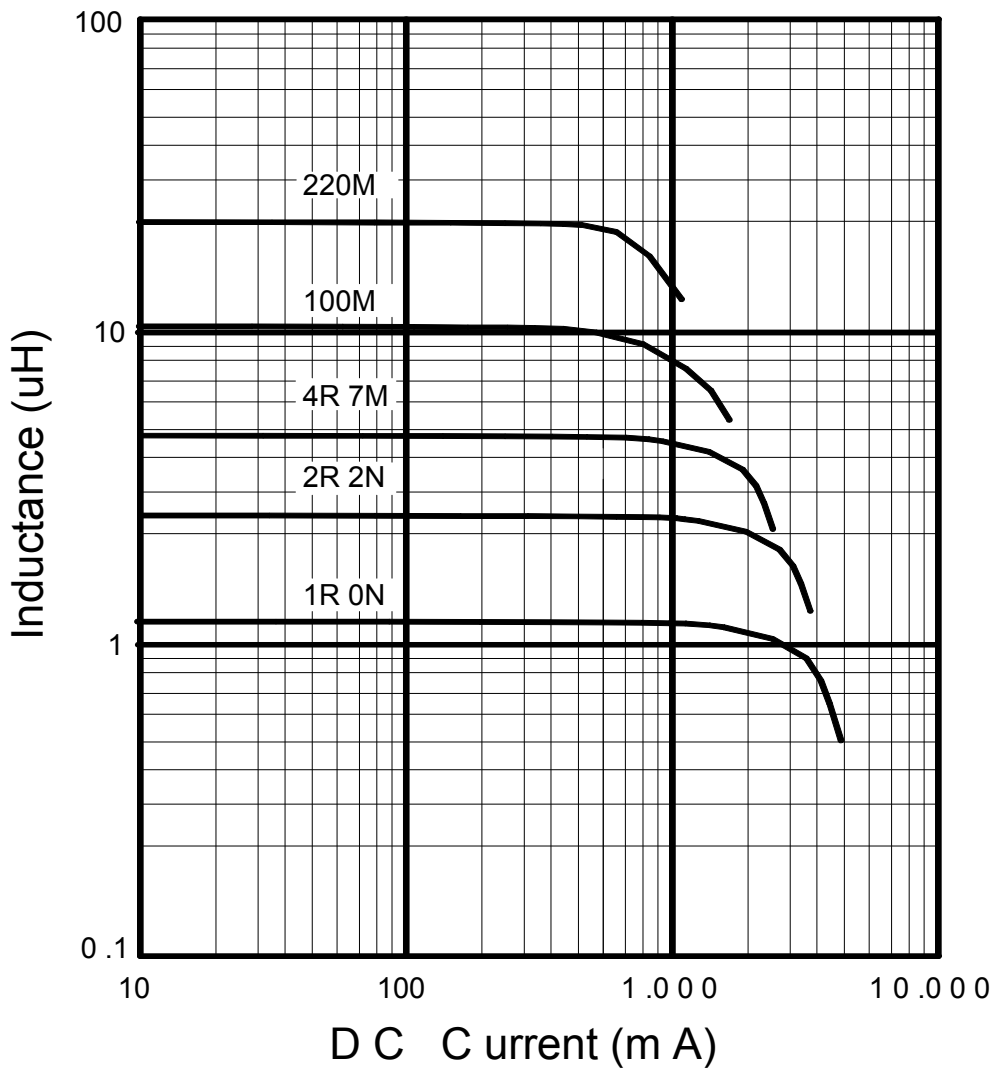


(Magnetic Shielded Type)

- | | | |
|--------------------------|---|----------|
| ϵ Ferrite core. | Ni-Zn ferrite | |
| $\&$ Winding wire | Polyurethane-copper wire | |
| ● Over-coating resin. | Epoxy resin, containing ferrite powder | |
| ○ Electrode | External electrode (substrate) | Ag |
| | External electrode (base plating) | Ni-Sn |
| | External electrode (top surface solder coating) | Sn-Ag-Cu |

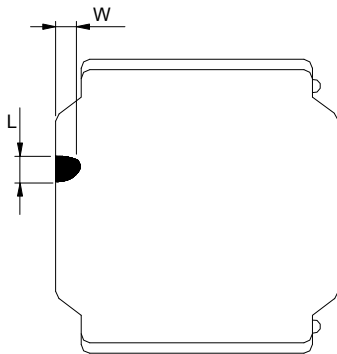
Electrical Curve

Inductance vs. D C C urrent



■ Core Chipping

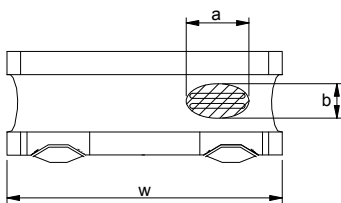
The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



L	W
1.5mmMax.	1.5mmMax.

■ Exposed wire tolerance limit of coating resin part on product side

Size of exposed wire occurring to coating resin is specified below.



- ☞ Width direction (dimension a): Acceptable when $a \leq w/2$
Nonconforming when $a > w/2$
- ☞ Length direction (dimension b): Dimension b is not specified.
- When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.