

SBOS167B – NOVEMBER 2000– REVISED JUNE 2002

microPower, Rail-to-Rail OPERATIONAL AMPLIFIERS

FEATURES

- **LOW I_Q : 20 μ A**
- **microSIZE PACKAGES: SC70-5**
SOT23-5, SOT23-8, and TSSOP-14
- **HIGH SPEED/POWER RATIO WITH**
BANDWIDTH: 350kHz
- **RAIL-TO-RAIL INPUT AND OUTPUT**
- **SINGLE SUPPLY: 2.3V to 5.5V**

APPLICATIONS

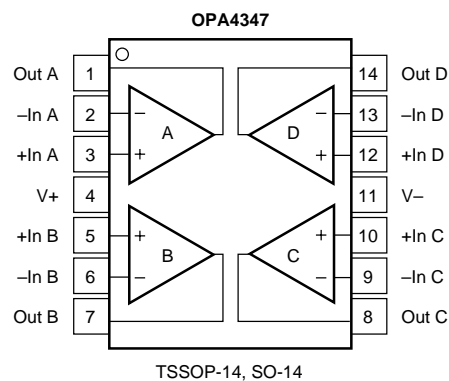
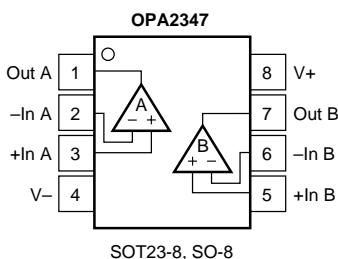
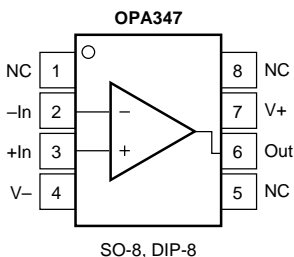
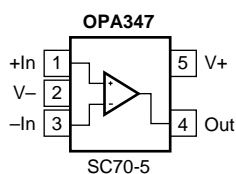
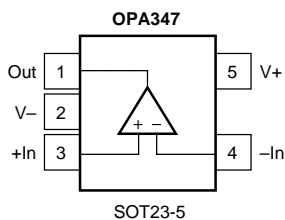
- **PORTABLE EQUIPMENT**
- **BATTERY-POWERED EQUIPMENT**
- **TWO-WIRE TRANSMITTERS**
- **SMOKE DETECTORS**
- **CO DETECTORS**

DESCRIPTION

The OPA347 is a *microPower*, low-cost operational amplifier available in *micropackages*. The OPA347 (single version) is available in the SC-70 and SOT23-5 packages. The OPA2347 (dual version) is available in the SOT23-8 package. Both are also available in the SO-8. The OPA347 is also available in the DIP-8. The OPA4347 (quad) is available in the SO-14 and the TSSOP-14.

The OPA347's small size and low power consumption (34 μ A per channel maximum) makes it ideal for portable and battery-powered applications. The input range of the OPA347 extends 200mV beyond the rails and the output range is within 5mV of the rails. The OPA347 also features an excellent speed/power ratio with a bandwidth of 350kHz.

The OPA347 can be operated with a single or dual power supply from 2.3V to 5.5V. All models are specified for operation from -55°C to 125°C .



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Supply Voltage, V+ to V-	7.5V
Signal Input Terminals, Voltage ⁽²⁾	(V-) – 0.5V to (V+) + 0.5V
Current ⁽²⁾	10mA
Output Short-Circuit ⁽³⁾	Continuous
Operating Temperature	–65°C to +150°C
Storage Temperature	–65°C to +150°C
Junction Temperature	150°C
Lead Temperature (soldering, 10s)	300°C

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only. Functional operation of the device at these conditions, or beyond the specified operating conditions, is not implied. (2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less. (3) Short-circuit to ground, one amplifier per package.



ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DESIGNATOR ⁽¹⁾	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER ⁽²⁾	TRANSPORT MEDIA
OPA347NA "	SOT23-5 "	DBV "	–55°C to +125°C "	A47 "	OPA347NA/250 OPA347NA/3K	Tape and Reel, 250 Tape and Reel, 3000
OPA347PA OPA347UA "	DIP-8 SO-8 "	P D "	–55°C to +125°C –55°C to +125°C "	OPA347PA OPA347UA "	OPA347PA OPA347UA OPA347UA/2K5 OPA347SA/250 OPA347SA/3K	Rails Rails Tape and Reel, 2500 Tape and Reel, 250 Tape and Reel, 3000
OPA347SA "	SC-70 "	DCK "	–55°C to +125°C "	S47 "		
OPA2347EA "	SOT23-8 "	DCN "	–55°C to +125°C "	B47 "	OPA2347EA/250 OPA2347EA/3K	Tape and Reel, 250 Tape and Reel, 3000
OPA2347UA "	SO-8 "	D "	–55°C to +125°C "	OPA2347UA "	OPA2347UA OPA2347UA/2K5	Rails Tape and Reel, 2500
OPA4347EA "	TSSOP-14 "	PW "	–55°C to +125°C "	OPA4347EA "	OPA4347EA/250 OPA4347EA/2K5	Tape and Reel, 250 Tape and Reel, 2500
OPA4347UA "	SO-14 "	D "	–55°C to +125°C "	OPA4347UA "	OPA4347UA OPA4347UA/2K5	Rails Tape and Reel, 2500

NOTES: (1) For the most current specifications and package information, refer to our website at www.ti.com. (2) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /3K indicates 3000 devices per reel). Ordering 3000 pieces of "OPA347NA/3K" will get a single 3000-piece Tape and Reel.

ELECTRICAL CHARACTERISTICS: $V_S = 2.5V$ to $5.5V$

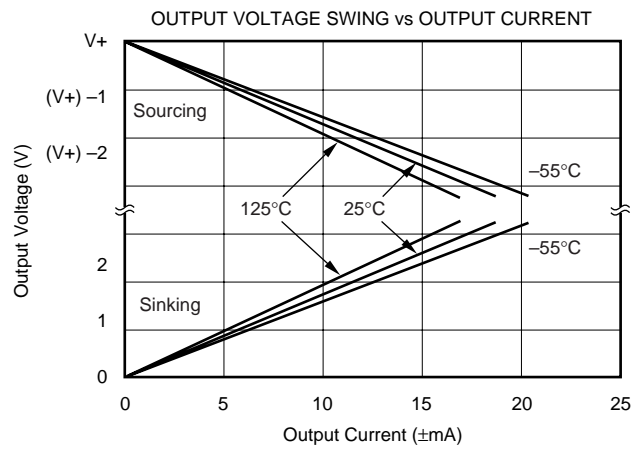
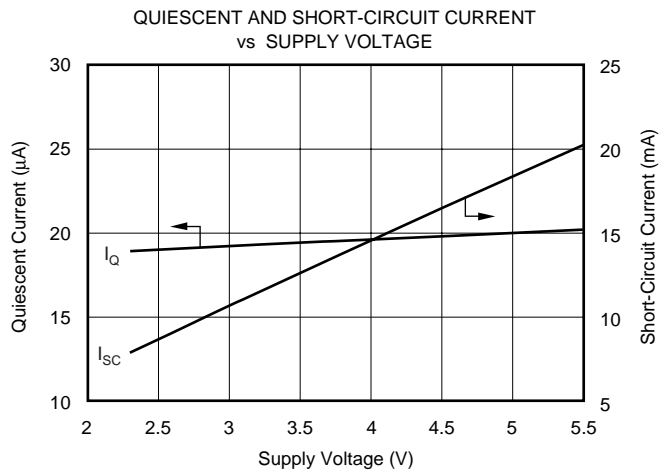
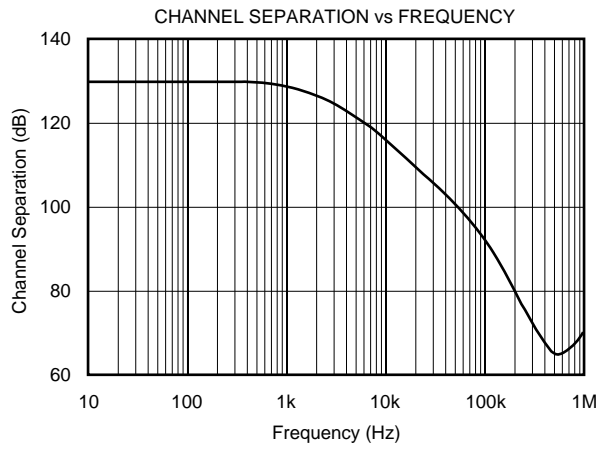
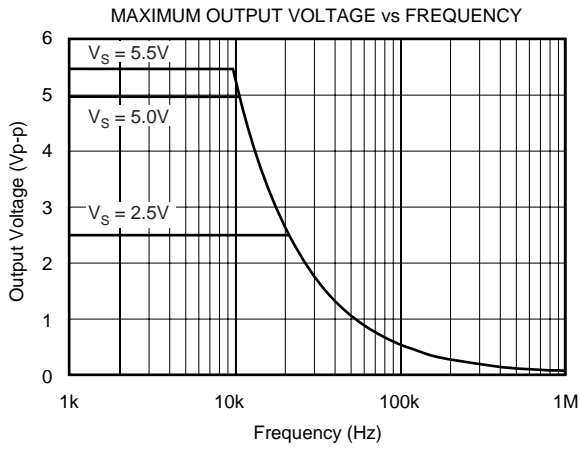
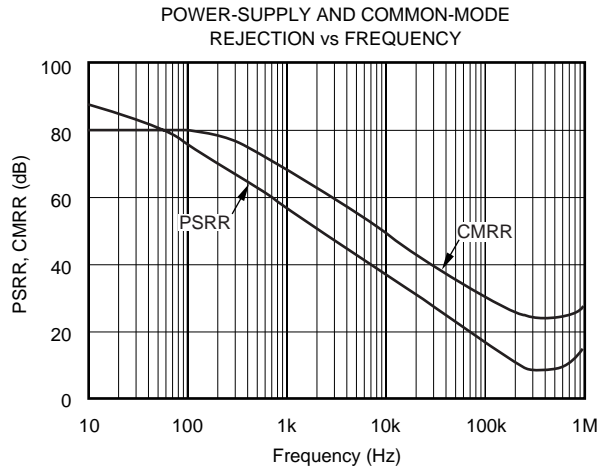
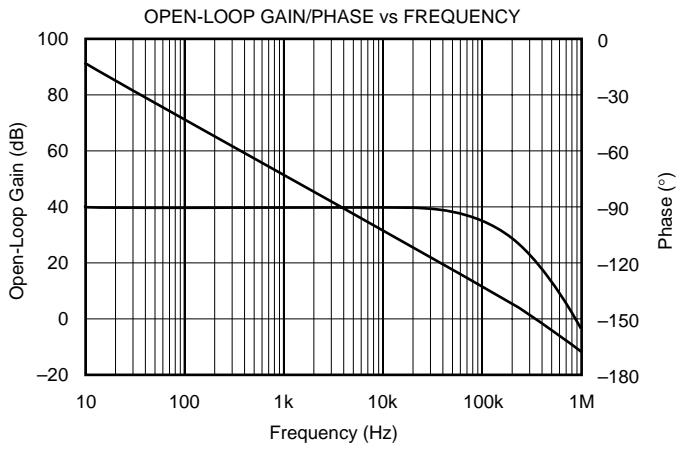
Boldface limits apply over the specified temperature range, $T_A = -55^{\circ}C$ to $+125^{\circ}C$

At $T_A = +25^{\circ}C$, $R_L = 100k\Omega$ connected to $V_S/2$ and $V_{OUT} = V_S/2$, unless otherwise noted.

PARAMETER	CONDITION	OPA347NA, UA, PA, SA OPA2347EA, UA OPA4347EA, UA			UNITS
		MIN	TYP	MAX	
OFFSET VOLTAGE Input Offset Voltage V_{OS} Over Temperature Drift dV_{OS}/dT vs Power Supply PSRR Over Temperature Channel Separation, dc	$V_S = 5.5V, V_{CM} = (V-) + 0.8V$ $V_S = 2.5V$ to $5.5V, V_{CM} < (V+) - 1.7V$ $V_S = 2.5V$ to $5.5V, V_{CM} < (V+) - 1.7V$ $f = 1kHz$		2	6	mV
			2	7	mV
			3		$\mu V/^{\circ}C$
			60	175	$\mu V/V$
		0.3		$\mu V/V$	
		128		dB	
INPUT VOLTAGE RANGE Common-Mode Voltage Range V_{CM} Common-Mode Rejection Ratio CMRR over Temperature over Temperature	$V_S = 5.5V, (V-) - 0.2V < V_{CM} < (V+) - 1.7V$ $V_S = 5.5V, V- < V_{CM} < (V+) - 1.7V$ $V_S = 5.5V, (V-) - 0.2V < V_{CM} < (V+) + 0.2V$ $V_S = 5.5V, V- < V_{CM} < V+$	$(V-) - 0.2$		$(V+) + 0.2$	V
		70	80		dB
		66			dB
		54	70		dB
		48		dB	
INPUT BIAS CURRENT Input Bias Current I_B Input Offset Current I_{OS}			± 0.5	± 10	pA
			± 0.5	± 10	pA
INPUT IMPEDANCE Differential Common-Mode			$10^{13} \parallel 3$		$\Omega \parallel pF$
			$10^{13} \parallel 6$		$\Omega \parallel pF$
NOISE Input Voltage Noise, $f = 0.1Hz$ to $10Hz$ Input Voltage Noise Density, $f = 1kHz$ e_n Input Current Noise Density, $f = 1kHz$ i_n	$V_{CM} < (V+) - 1.7V$		12		$\mu Vp-p$
			60		nV/\sqrt{Hz}
			0.7		fA/\sqrt{Hz}
OPEN-LOOP GAIN Open-Loop Voltage Gain A_{OL} over Temperature over Temperature A_{OL} (SC-70 only)	$V_S = 5.5V, R_L = 100k\Omega, 0.015V < V_O < 5.485V$ $V_S = 5.5V, R_L = 100k\Omega, 0.015V < V_O < 5.485V$ $V_S = 5.5V, R_L = 5k\Omega, 0.125V < V_O < 5.375V$ $V_S = 5.5V, R_L = 5k\Omega, 0.125V < V_O < 5.375V$ $V_S = 5.5V, R_L = 5k\Omega, 0.125V < V_O < 5.375V$	100	115		dB
		88			dB
		100	115		dB
		88			dB
		96	115		dB
OUTPUT Voltage Output Swing from Rail over Temperature over Temperature Short-Circuit Current I_{SC} Capacitive Load Drive C_{LOAD}	$R_L = 100k\Omega, A_{OL} > 100dB$ $R_L = 100k\Omega, A_{OL} > 88dB$ $R_L = 5k\Omega, A_{OL} > 100dB$ $R_L = 5k\Omega, A_{OL} > 88dB$		5	15	mV
				15	mV
			90	125	mV
				125	mV
		± 17		mA	
		See Typical Characteristics			
FREQUENCY RESPONSE Gain-Bandwidth Product GBW Slew Rate SR Settling Time, 0.1% t_s 0.01% Overload Recovery Time	$C_L = 100pF$ $G = +1$ $V_S = 5V, 2V$ Step, $G = +1$ $V_S = 5V, 2V$ Step, $G = +1$ $V_{IN} \cdot \text{Gain} = V_S$		350		kHz
			0.17		$V/\mu s$
			21		μs
			27		μs
			23		μs
POWER SUPPLY Specified Voltage Range V_S Minimum Operating Voltage Minimum Operating Voltage (OPA347SA) Quiescent Current (per amplifier) I_Q over Temperature	$I_Q = 0$	2.5		5.5	V
			2.3		V
			2.4		V
			20	34	μA
			38	μA	
TEMPERATURE RANGE Specified Range Operating Range Storage Range Thermal Resistance θ_{JA} SOT23-5 Surface-Mount SOT23-8 Surface-Mount SO-8 Surface-Mount SO-14 Surface-Mount TSSOP-14 Surface-Mount DIP-8 SC70-5 Surface Mount		-55		125	$^{\circ}C$
		-65		150	$^{\circ}C$
		-65		150	$^{\circ}C$
			200		$^{\circ}C/W$
			150		$^{\circ}C/W$
			150		$^{\circ}C/W$
			100		$^{\circ}C/W$
			100		$^{\circ}C/W$
			100		$^{\circ}C/W$
			100		$^{\circ}C/W$
			250		$^{\circ}C/W$

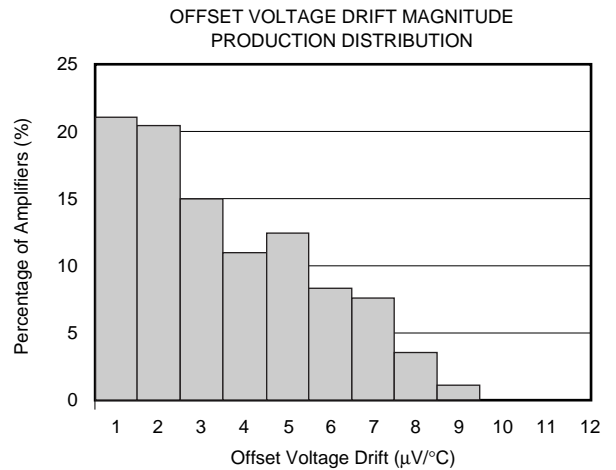
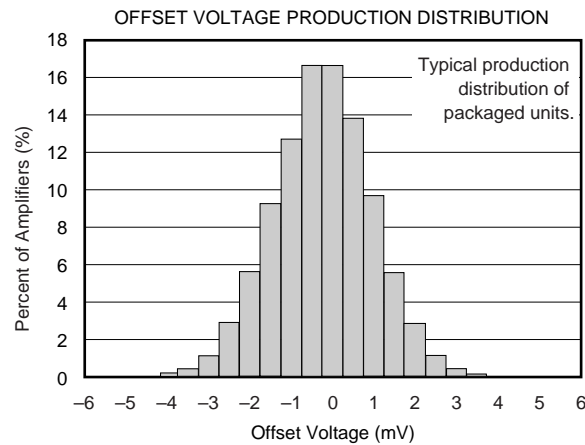
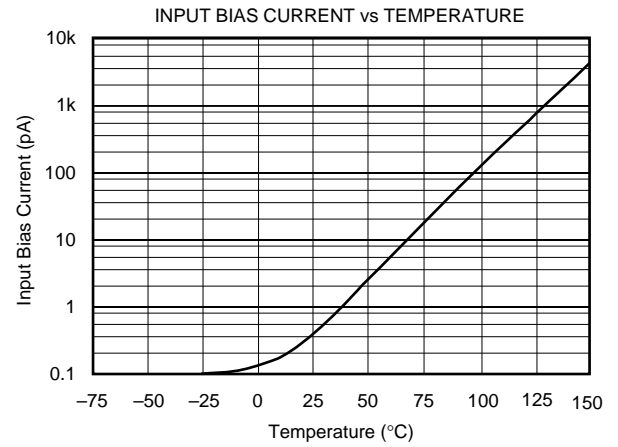
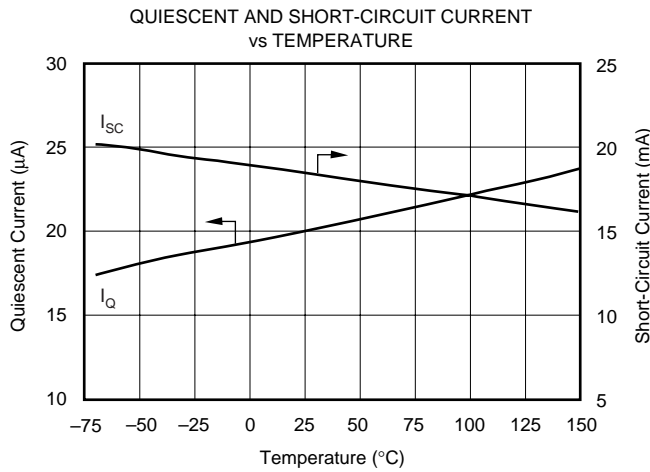
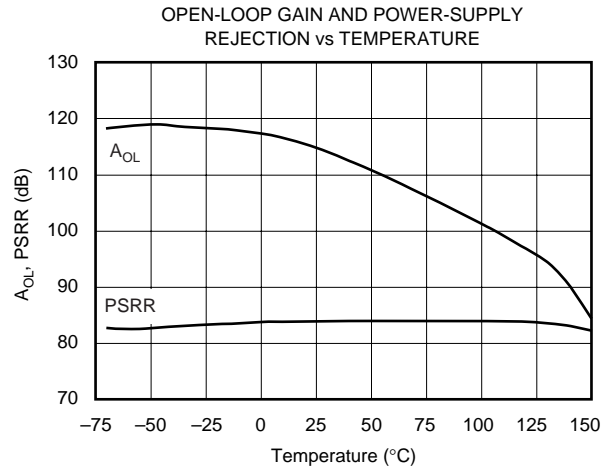
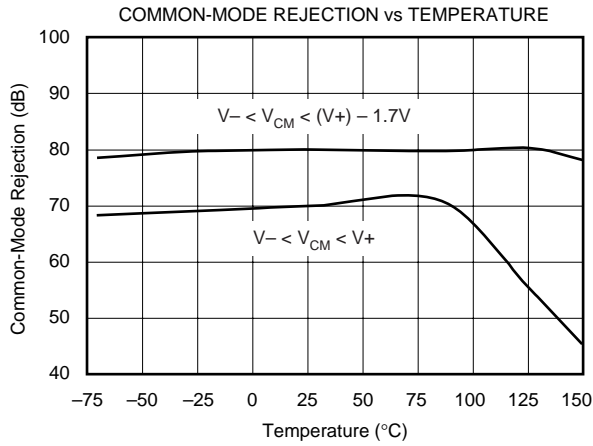
TYPICAL CHARACTERISTICS

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 100\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



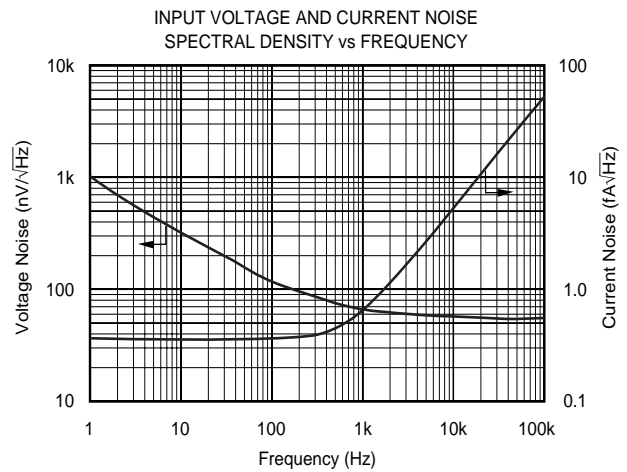
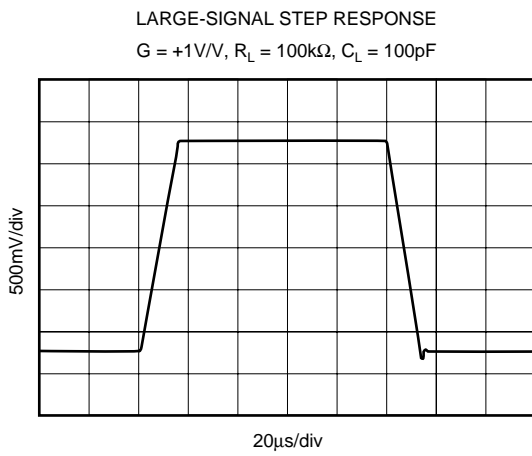
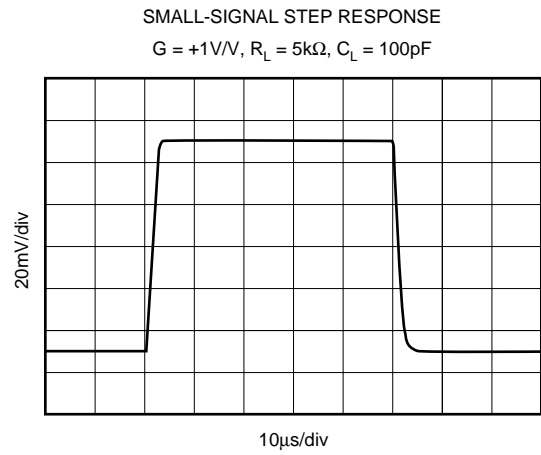
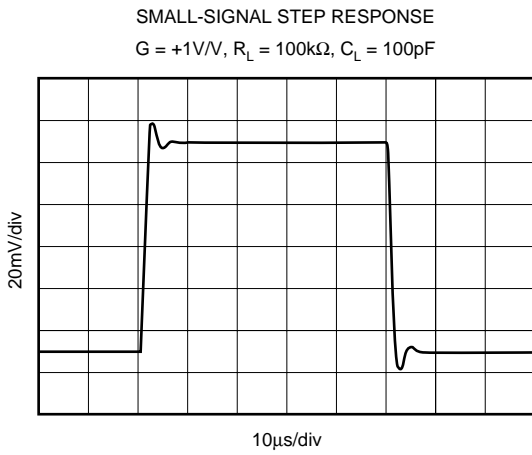
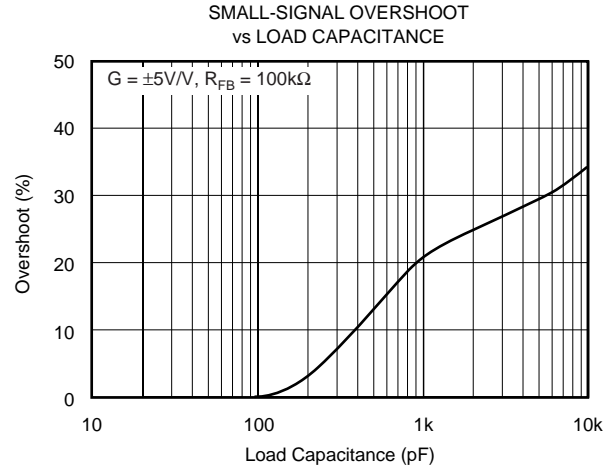
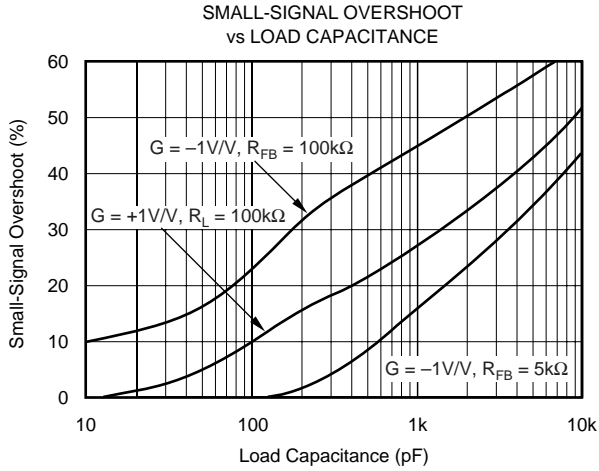
TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 100\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



TYPICAL CHARACTERISTICS (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, and $R_L = 100\text{k}\Omega$ connected to $V_S/2$, unless otherwise noted.



APPLICATIONS INFORMATION

OPA347 series op amps are unity-gain stable and can operate on a single supply, making them highly versatile and easy to use.

Rail-to-rail input and output swing significantly increases dynamic range, especially in low supply applications. Figure 1 shows the input and output waveforms for the OPA347 in unity-gain configuration. Operation is from $V_S = +5V$ with a $100k\Omega$ load connected to $V_S/2$. The input is a $5V_{p-p}$ sinusoid. Output voltage is approximately $4.995V_{p-p}$.

Power-supply pins should be bypassed with $0.01\mu F$ ceramic capacitors.

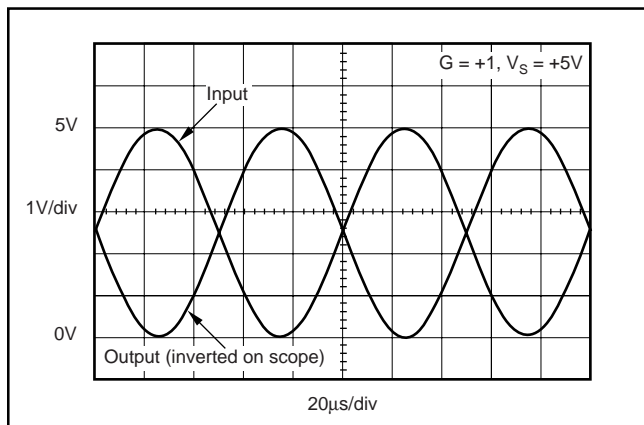


FIGURE 1. Rail-to-Rail Input and Output.

OPERATING VOLTAGE

OPA347 series op amps are fully specified and guaranteed from $2.5V$ to $5.5V$. In addition, many specifications apply from $-55^\circ C$ to $+125^\circ C$. Parameters that vary significantly with operating voltages or temperature are shown in the Typical Characteristics.

RAIL-TO-RAIL INPUT

The input common-mode voltage range of the OPA347 series extends $200mV$ beyond the supply rails. This is achieved with a complementary input stage—an N-channel input differential pair in parallel with a P-channel differential pair, as shown in Figure 2. The N-channel pair is active for input voltages close to the positive rail, typically $(V+) - 1.3V$ to $200mV$ above the positive supply, while the P-channel pair is on for inputs from $200mV$ below the negative supply to approximately $(V+) - 1.3V$. There is a small transition region, typically $(V+) - 1.5V$ to $(V+) - 1.1V$, in which both pairs are on. This $400mV$ transition region can vary $300mV$ with process variation. Thus, the transition region (both stages on) can range from $(V+) - 1.65V$ to $(V+) - 1.25V$ on the low end, up to $(V+) - 1.35V$ to $(V+) - 0.95V$ on the high end. Within the $400mV$ transition region PSRR, CMRR, offset voltage, and offset drift may be degraded compared to operation outside this region. For more information on designing with rail-to-rail input op amps, see Figure 3, "Design Optimization with Rail-to-Rail Input Op Amps."

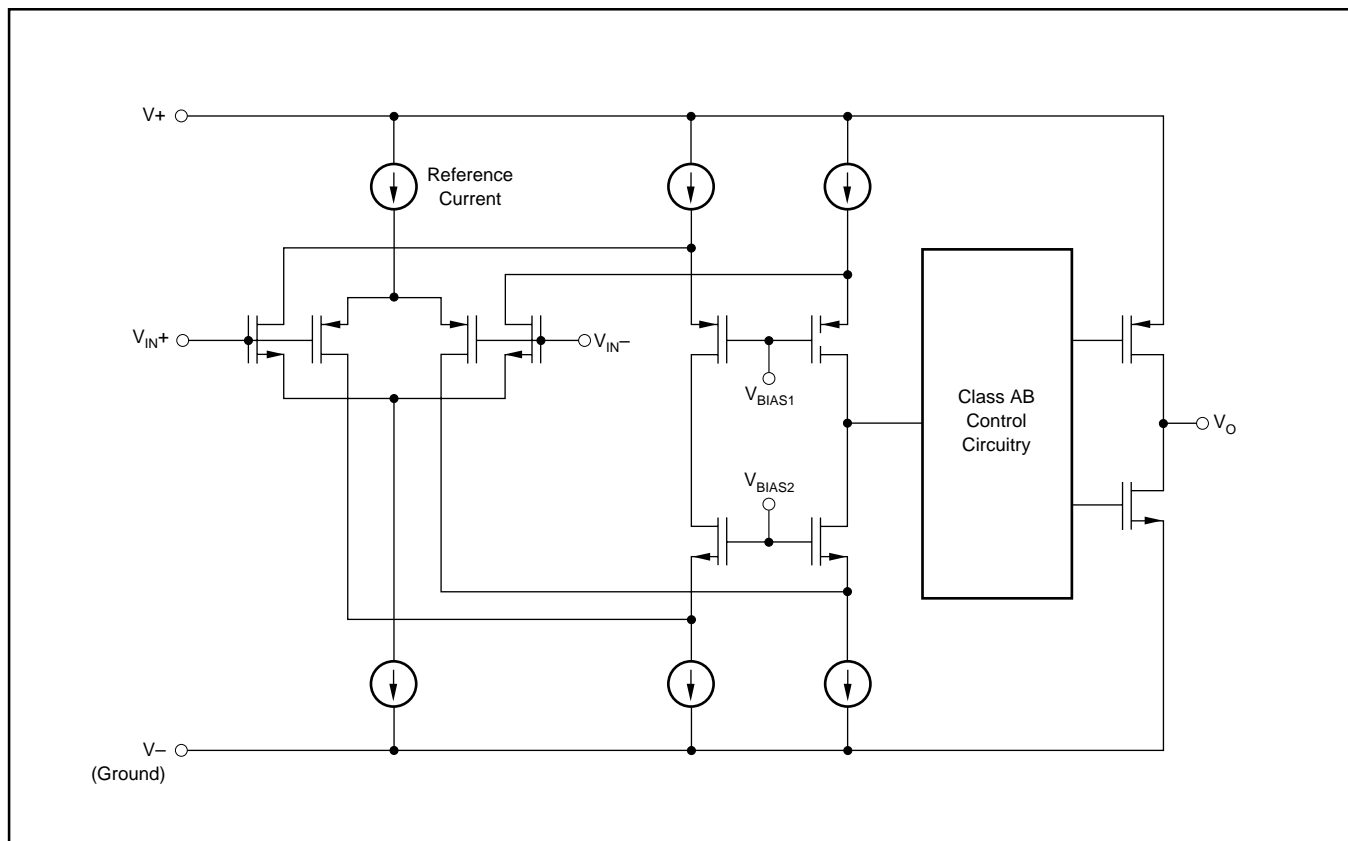


FIGURE 2. Simplified Schematic.

DESIGN OPTIMIZATION WITH RAIL-TO-RAIL INPUT OP AMPS

Rail-to-rail op amps can be used in virtually any op amp configuration. To achieve optimum performance, however, applications using these special double-input-stage op amps may benefit from consideration of their special behavior.

In many applications, operation remains within the common-mode range of only one differential input pair. However some applications exercise the amplifier through the transition region of both differential input stages. A small discontinuity may occur in this transition. Careful selection of the circuit configuration, signal levels and biasing can often avoid this transition region.

With a unity-gain buffer, for example, signals will traverse this transition at approximately 1.3V below $V+$ supply and may exhibit a small discontinuity at this point.

The common-mode voltage of the noninverting amplifier is equal to the input voltage. If the input signal always remains less than the transition voltage, no discontinuity will be created. The closed-loop gain of this configuration can still produce a rail-to-rail output.

Inverting amplifiers have a constant common-mode voltage equal to V_B . If this bias voltage is constant, no discontinuity will be created. The bias voltage can generally be chosen to avoid the transition region.

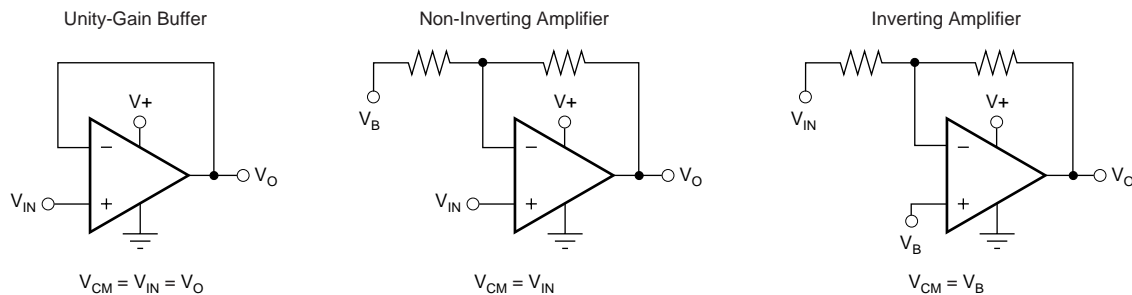


FIGURE 3. Design Optimization with Rail-to-Rail Input Op Amps.

COMMON-MODE REJECTION

The CMRR for the OPA347 is specified in several ways so the best match for a given application may be used. First, the CMRR of the device in the common-mode range below the transition region ($V_{CM} < (V+) - 1.7V$) is given. This specification is the best indicator of the capability of the device when the application requires use of one of the differential input pairs. Second, the CMRR at $V_S = 5.5V$ over the entire common-mode range is specified.

INPUT VOLTAGE

The input common-mode range extends from $(V-) - 0.2V$ to $(V+) + 0.2V$. For normal operation, inputs should be limited to this range. The absolute maximum input voltage is 500mV beyond the supplies. Inputs greater than the input common-mode range but less than the maximum input voltage, while not valid, will not cause any damage to the op amp. Furthermore, if input current is limited the inputs may go beyond the power supplies without phase inversion, as shown in Figure 4, unlike some other op amps.

Normally, input currents are 0.4pA. However, large inputs (greater than 500mV beyond the supply rails) can cause excessive current to flow in or out of the input pins. Therefore, as well as keeping the input voltage below the maximum rating, it is also important to limit the input current to less than 10mA. This is easily accomplished with an input resistor, as shown in Figure 5.

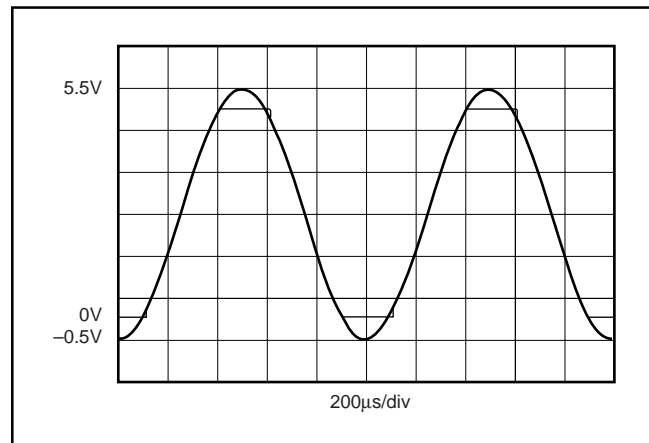


FIGURE 4. OPA347—No Phase Inversion with Inputs Greater than the Power-Supply Voltage.

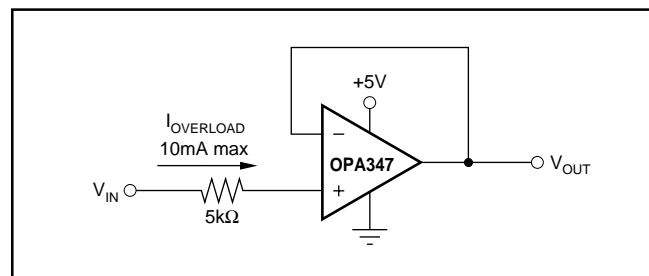


FIGURE 5. Input Current Protection for Voltages Exceeding the Supply Voltage.

RAIL-TO-RAIL OUTPUT

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. This output stage is capable of driving $5\text{k}\Omega$ loads connected to any potential between $V+$ and ground. For light resistive loads ($> 100\text{k}\Omega$), the output voltage can typically swing to within 5mV from supply rail. With moderate resistive loads ($10\text{k}\Omega$ to $50\text{k}\Omega$), the output can swing to within a few tens of millivolts from the supply rails while maintaining high open-loop gain (see the typical characteristic “Output Voltage Swing vs Output Current”).

CAPACITIVE LOAD AND STABILITY

The OPA347 in a unity-gain configuration can directly drive up to 250pF pure capacitive load. Increasing the gain enhances the amplifier’s ability to drive greater capacitive loads (see the characteristic curve “Small-Signal Overshoot vs Capacitive Load”). In unity-gain configurations, capacitive load drive can be improved by inserting a small (10Ω to 20Ω) resistor, R_S , in series with the output, as shown in Figure 6. This significantly reduces ringing while maintaining DC performance for purely capacitive loads. However, if there is a resistive load in parallel with the capacitive load, a voltage divider is created, introducing a Direct Current (DC) error at the output and slightly reducing the output swing. The error introduced is proportional to the ratio R_S/R_L , and is generally negligible.

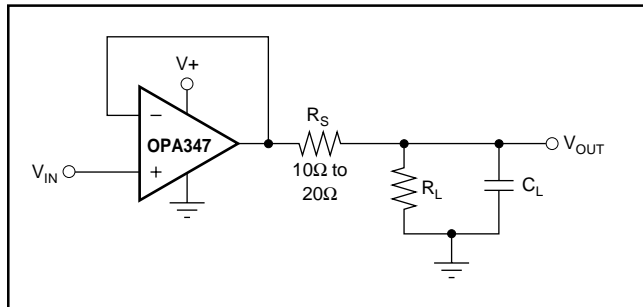


FIGURE 6. Series Resistor in Unity-Gain Buffer Configuration Improves Capacitive Load Drive.

In unity-gain inverter configuration, phase margin can be reduced by the reaction between the capacitance at the op amp input, and the gain setting resistors, thus degrading capacitive load drive. Best performance is achieved by using small valued resistors. For example, when driving a 500pF

load, reducing the resistor values from $100\text{k}\Omega$ to $5\text{k}\Omega$ decreases overshoot from 40% to 8% (see the characteristic curve “Small-Signal Overshoot vs Load Capacitance”). However, when large valued resistors can not be avoided, a small (4pF to 6pF) capacitor, C_{FB} , can be inserted in the feedback, as shown in Figure 7. This significantly reduces overshoot by compensating the effect of capacitance, C_{IN} , which includes the amplifier’s input capacitance and PC board parasitic capacitance.

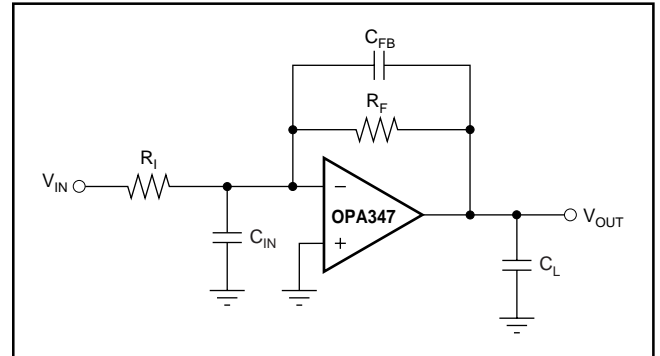


FIGURE 7. Adding A Feedback Capacitor In the Unity-Gain Inverter Configuration Improves Capacitive Load.

DRIVING ADCs

The OPA347 series op amps are optimized for driving medium-speed sampling Analog-to-Digital Converters (ADCs). The OPA347 op amps buffer the ADC’s input capacitance and resulting charge injection while providing signal gain.

See Figures 8 for the OPA347 in a basic noninverting configuration driving the ADS7822. The ADS7822 is a 12-bit, *microPower* sampling converter in the MSOP-8 package. When used with the low-power, miniature packages of the OPA347, the combination is ideal for space-limited, low-power applications. In this configuration, an RC network at the ADC input can be used to provide for anti-aliasing filter and charge injection current.

See Figure 9 for the OPA2347 driving an ADS7822 in a speech bandpass filtered data acquisition system. This small, low-cost solution provides the necessary amplification and signal conditioning to interface directly with an electret microphone. This circuit will operate with $V_S = 2.7\text{V}$ to 5V with less than $250\mu\text{A}$ typical quiescent current.

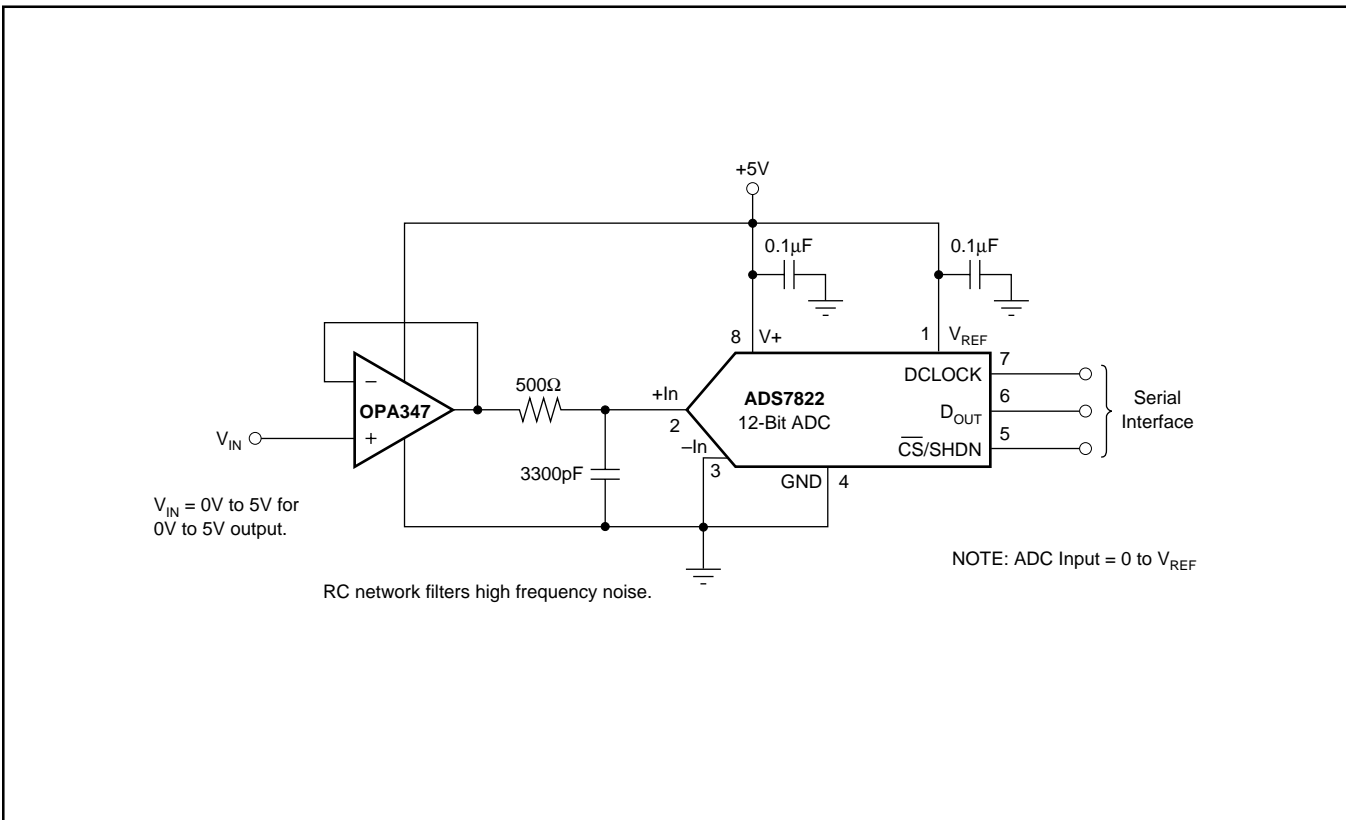


FIGURE 8. OPA347 in Noninverting Configuration Driving ADS7822.

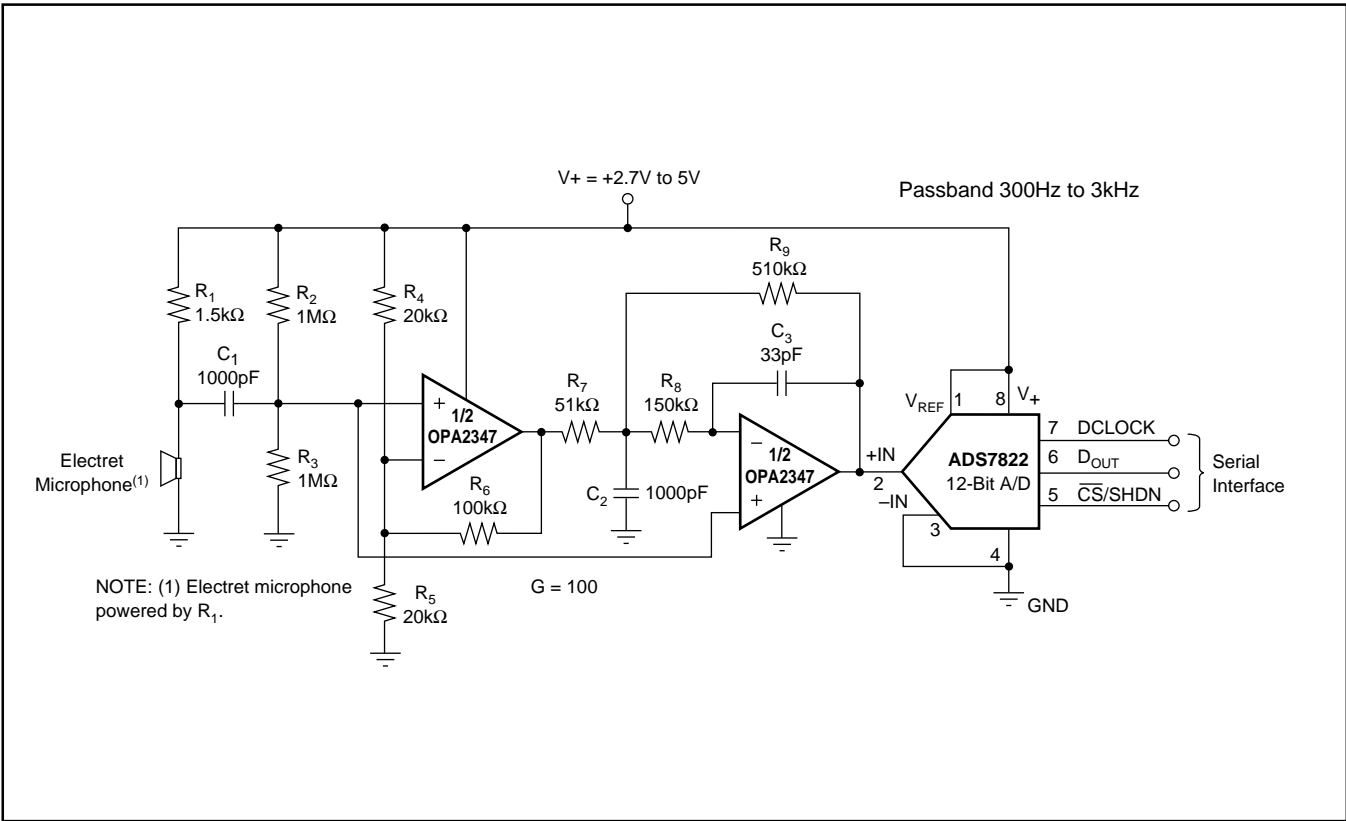
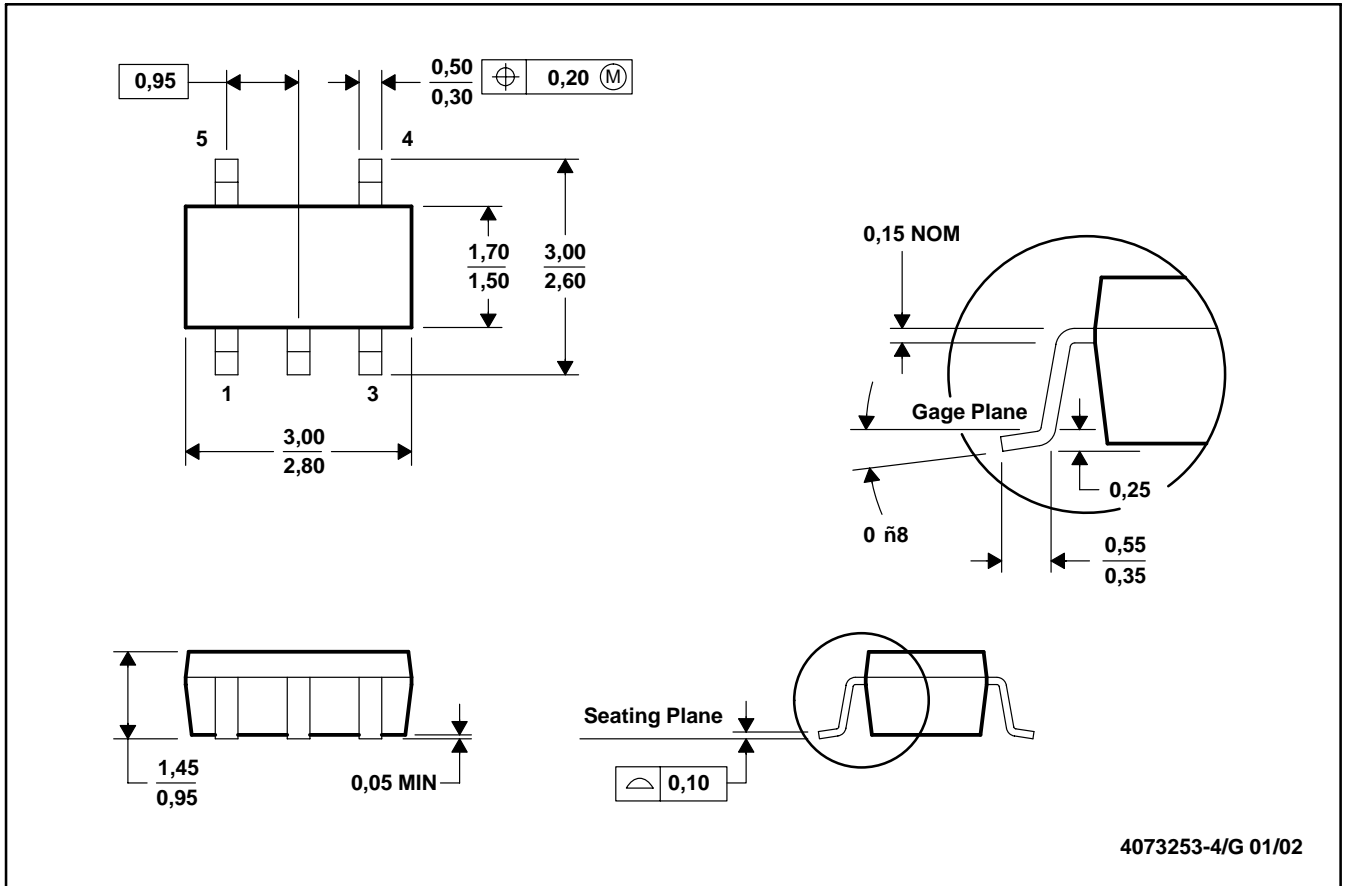


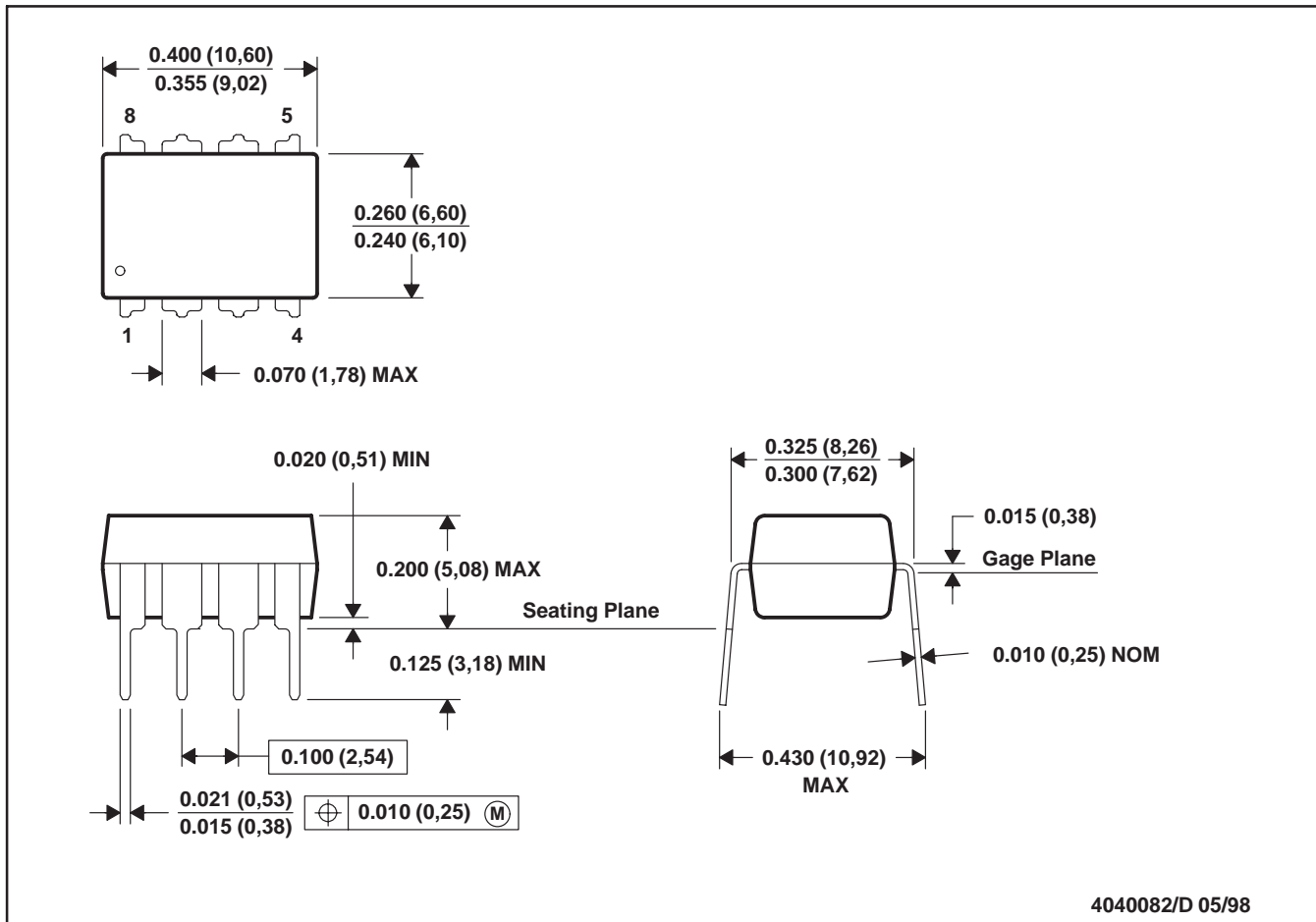
FIGURE 9. Speech Bandpass Filtered Data Acquisition System.



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion.
 - D. Falls within JEDEC MO-178

P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



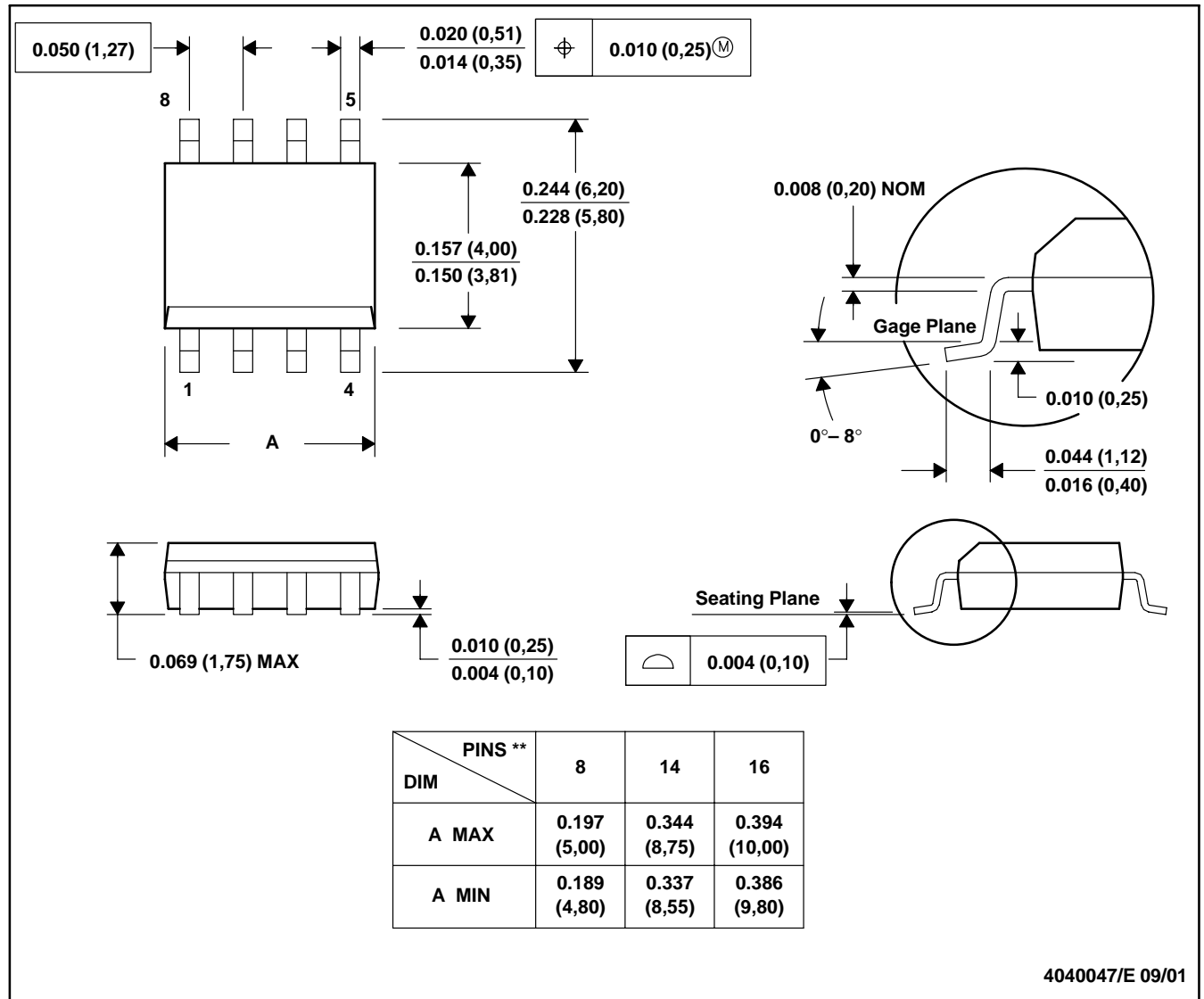
4040082/D 05/98

- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Falls within JEDEC MS-001

D (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

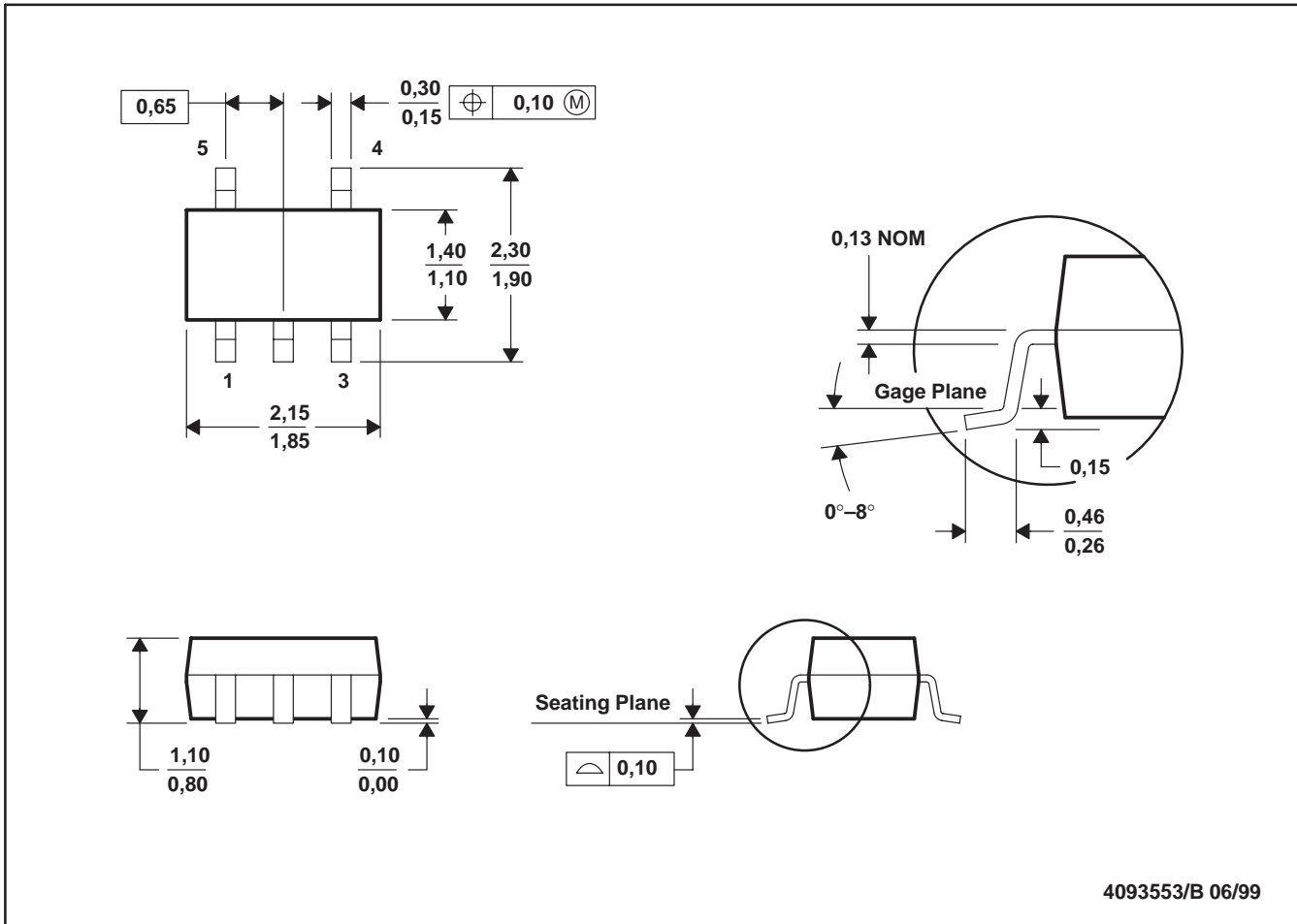
8 PINS SHOWN



- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).
 D. Falls within JEDEC MS-012

DCK (R-PDSO-G5)

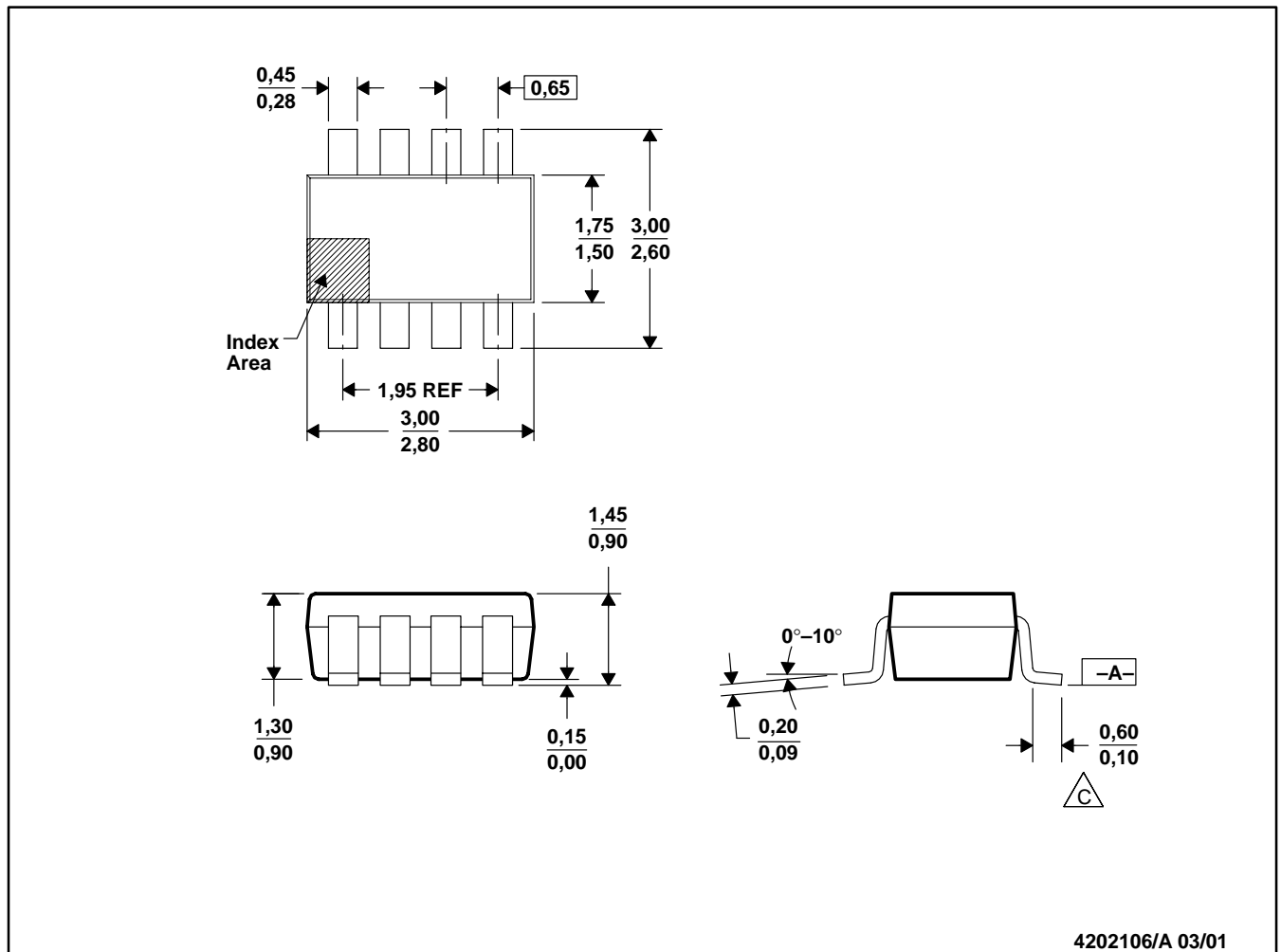
PLASTIC SMALL-OUTLINE




- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion.
 - D. Falls within JEDEC MO-203

DCN (R-PDSO-G8)

PLASTIC SMALL-OUTLINE



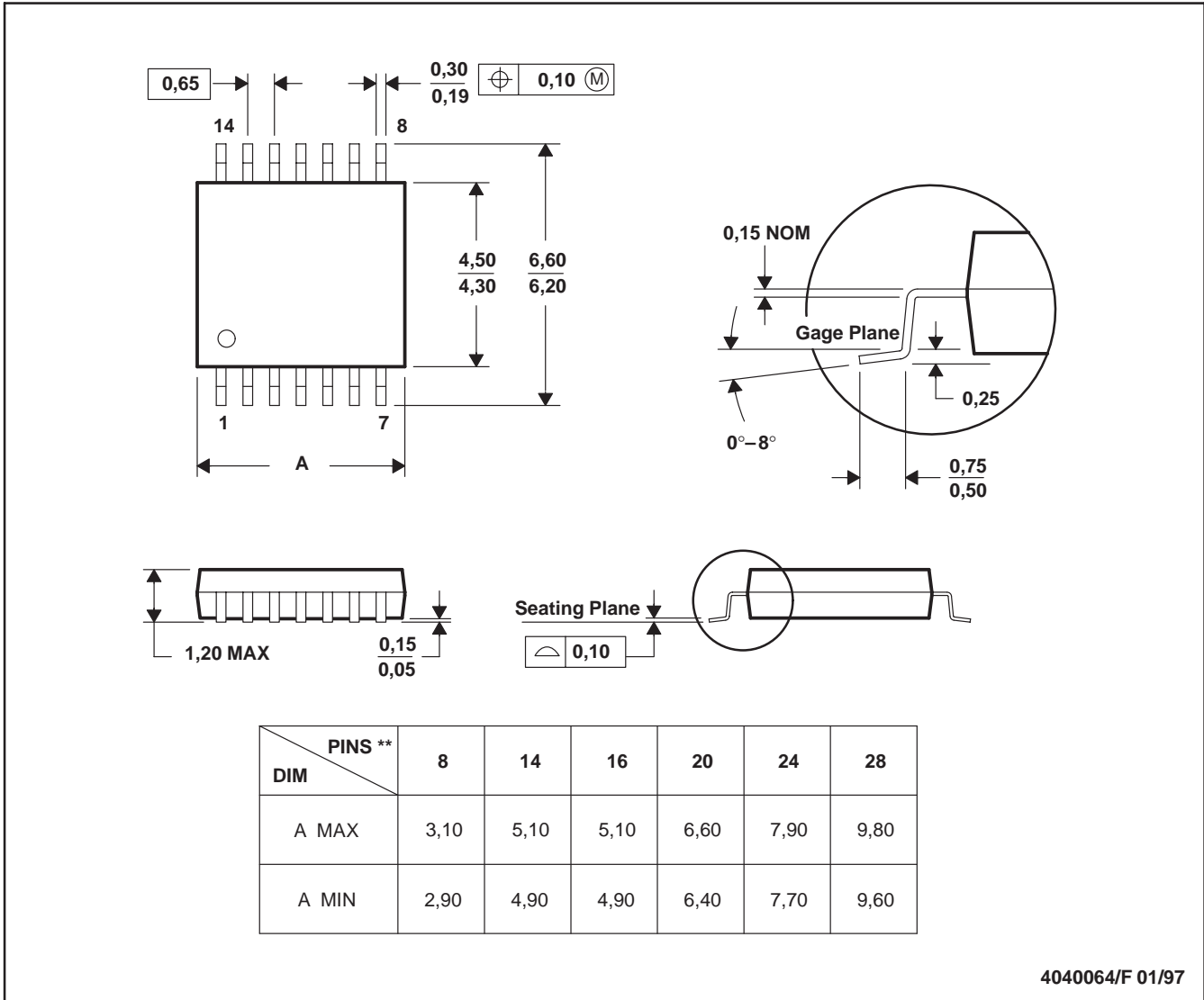
4202106/A 03/01

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 -  C. Foot length measured reference to flat foot surface parallel to Datum A.
 - D. Package outline exclusive of mold flash, metal burr and dambar protrusion/intrusion.
 - E. Package outline inclusive of solder plating.
 - F. A visual index feature must be located within the cross-hatched area.

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 D. Falls within JEDEC MO-153

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Mailing Address:

Texas Instruments
Post Office Box 655303
Dallas, Texas 75265