Preferred Device

Schottky Power Rectifier

Surface Mount Power Package

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes, in surface mount applications where compact size and weight are critical to the system.

Features

- Very Low Forward Voltage Drop (0.395 Volts Max @ 1.0 A, T_J = 25°C)
- Small Compact Surface Mountable Package with J-Bend Leads
- Highly Stable Oxide Passivated Junction
- Guard-Ring for Stress Protection
- Pb-Free Package is Available

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Cathode Polarity Band



ON Semiconductor®

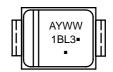
http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 1.0 AMPERE 30 VOLTS



SMB CASE 403A PLASTIC

MARKING DIAGRAM



1BL3 = Specific Device Code A = Assembly Location

Y = Year WW = Work Week ■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device | Package | Shipping [†] | |
|-------------|------------------|-----------------------|--|
| MBRS130LT3 | SMB | 2500/Tape & Reel | |
| MBRS130LT3G | SMB (Pb-Free) | 2500/Tape & Reel | |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|--|-------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 30 | V |
| Average Rectified Forward Current $T_L = 120^{\circ}C$ $T_L = 110^{\circ}C$ | I _{F(AV)} | 1.0 2.0 | А |
| Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz) | I _{FSM} | 40 | А |
| Operating Junction Temperature | TJ | -65 to +125 | °C |

THERMAL CHARACTERISTICS

| Rating | Symbol | Value | Unit |
|---|-----------------|-------|------|
| Thermal Resistance, | $\Psi_{\sf JL}$ | | °C/W |
| Junction-to-Lead | | 12 | |
| Thermal Resistance, | $R_{\theta JA}$ | | °C/W |
| Junction-to-Ambient (T _A = 25°C, Min Pad, 1 oz copper) | | 228.8 | |
| Junction-to-Ambient (T _A = 25°C, 1" Pad, 1 oz copper) | | 71.3 | |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
|---|----------------|-------|------|
| Maximum Instantaneous Forward Voltage (Note 1) | V _F | | V |
| $(i_F = 1.0 \text{ A}, T_J = 25^{\circ}\text{C})$ | | 0.395 | |
| $(i_F = 2.0 \text{ A}, T_J = 25^{\circ}\text{C})$ | | 0.445 | |
| Maximum Instantaneous Reverse Current (Note 1) | I _R | | mA |
| (Rated dc Voltage, T _J = 25°C) | | 1.0 | |
| (Rated dc Voltage, $T_J = 100^{\circ}C$) | | 10 | |

^{1.} Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2%.

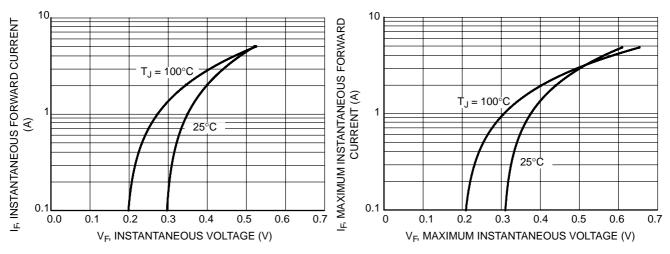


Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage

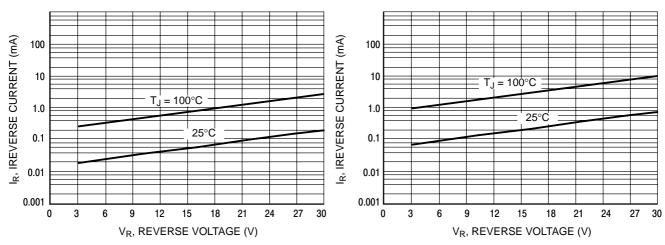


Figure 3. Typical Reverse Leakage Current

Figure 4. Typical Maximum Reverse Leakage Curent

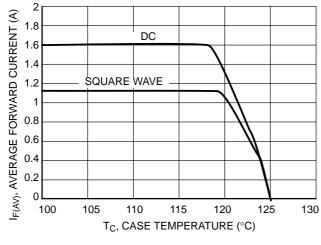


Figure 5. Current Derating (Case)

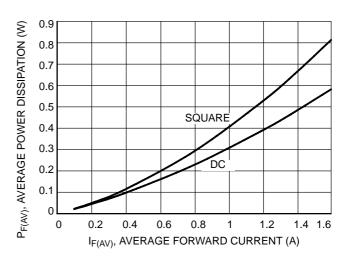


Figure 6. Typical Power Dissipation

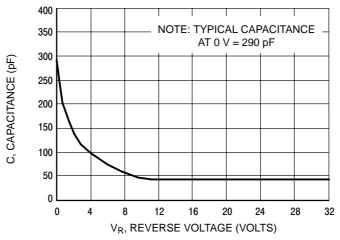


Figure 7. Typical Capacitance

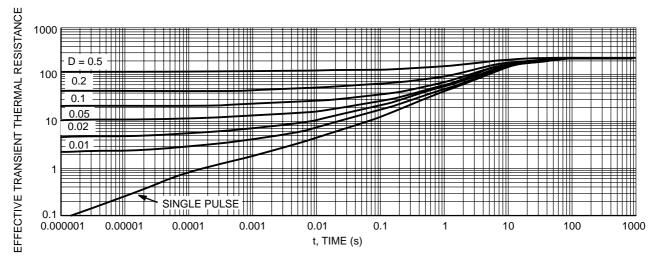


Figure 8. Thermal Response, Min Pad

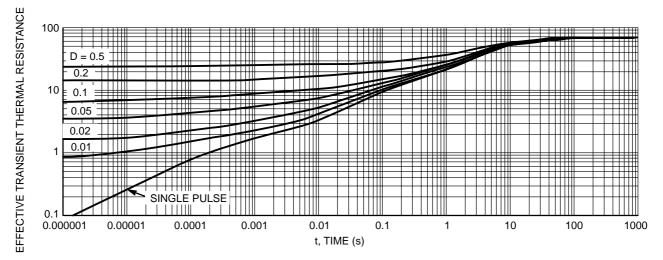


Figure 9. Thermal Response, 1 Inch Pad

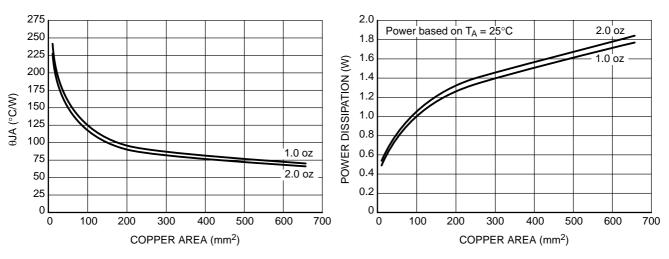


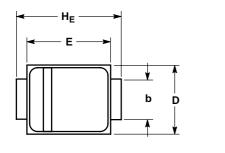
Figure 10. Thermal Resistance vs. Copper Area

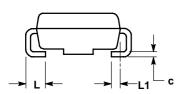
Figure 11. Power Dissipation vs. Copper Area

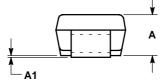
PACKAGE DIMENSIONS

SMB

PLASTIC PACKAGE CASE 403A-03 ISSUE F





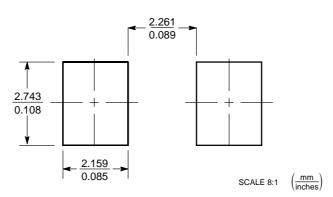


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

| | MILLIMETERS | | | INCHES | | |
|-----|-------------|----------|------|--------|-----------|-------|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX |
| Α | 1.90 | 2.13 | 2.45 | 0.075 | 0.084 | 0.096 |
| A1 | 0.05 | 0.10 | 0.20 | 0.002 | 0.004 | 0.008 |
| b | 1.96 | 2.03 | 2.20 | 0.077 | 0.080 | 0.087 |
| С | 0.15 | 0.23 | 0.31 | 0.006 | 0.009 | 0.012 |
| D | 3.30 | 3.56 | 3.95 | 0.130 | 0.140 | 0.156 |
| E | 4.06 | 4.32 | 4.60 | 0.160 | 0.170 | 0.181 |
| HE | 5.21 | 5.44 | 5.60 | 0.205 | 0.214 | 0.220 |
| L | 0.76 | 1.02 | 1.60 | 0.030 | 0.040 | 0.063 |
| L1 | | 0.51 REF | | | 0.020 REF | : |

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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