

# DC COMPONENTS CO., LTD.

### RECTIFIER SPECIALISTS

DB3 THRU DB6

## TECHNICAL SPECIFICATIONS OF BIDIRECTIONAL DIODE THYRISTORS (DIACS)

#### **FEATURES**

- \* Glass passivated three-layer for triggering thyristors.
- \* Low breakover current at breakover voltage.
- \* For use in thyristor phase-control circuit for lampdimming, universal-motor speed control and heat controls.

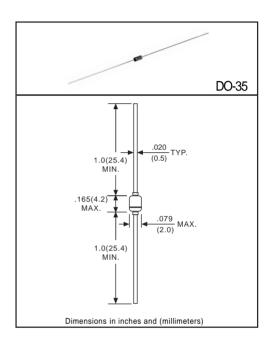
#### MECHANICAL DATA

\* Case: Glass sealed case

\* Terminals: MIL-STD-202E, Method 208 guaranteed

\* Mounting position: Any \* Weight: 0.15 gram Approx.

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS Rating at 25°C ambient tempature unless ohterwise specified Single phase, half wave 60 HZ, resistive or inductive load. For capacitive load, derate current by 20%.



## ABSOLUTE RATINGS(LIMITING VALUES)

PARAMETERS		VALUE				
	SYMBOL	DB3	DC34	DB4	DB6	UNITS
Power Dissipation on Printed Circuit (L=10mm) T <sub>A</sub> =25°C	Pc		1:	mW		
Repetitive Peak on-state Current tp=10μs f=100Hz	ITRM	2.0		1.6	А	
Maximum Lead Temperature for Soldering	Tstg/TJ	-40 to +125			°C	

#### **ELECTRICAL CHARACTERISTICS**

	TEST CONDITIONS		SYMBOL	VALUE					
PARAMETERS				DB3	DC34	DB4	DB6	UNITS	
Breakover Voltage (Note 2)	C=22nF (Note 2) See FIG.1	Min	Vво	28	30	35	56	Volts	
		Тур		32	34	40	60		
		Max		36	38	45	70		
Breakover Voltage Symmetry	C=22nF (Note 2) See FIG. 1	Max	I+VBOI-I-VBOI	A3			A3 A4		Volts
Dynamic Breakback Voltage (Note 1)	ΔI=(IBO to IF=10mA) See FIG. 1	Min	ΙΑΔVΙ	5			10	Volts	
Output Voltage (Note 1)	See FIG. 2	Min	Vo	5				Volts	
Breakover Current (Note 1)	C=22nF (Note 2)	Max	Іво	100				μΑ	
Rise time (Note 1)	See FIG. 3	Тур	tr	1.5				μs	
Leakage Current (Note 1)	V <sub>B</sub> =0.5 V <sub>B</sub> 0 max See FIG. 1	Max	Ів	10				μΑ	

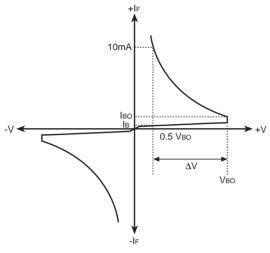
NOTE: 1. Electrical characteristics applicable in both forward and reverse directions.

2. Connected in parallel with the devices.

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## **RATING AND CHARACTERISTIC CURVES (DB3 THRU DB6)**

FIG.1 - VOLTAGE-CURRENT CHARACTERISTICS



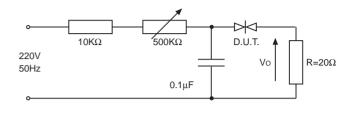


FIG.2 - TEST CIRCUIT FOR OUTPUT VOLTAGE

Vво(Т<sub>J</sub>)

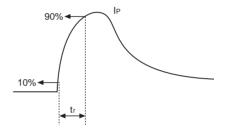


FIG.3 - TEST CIRCUIT SEE FIG.2 ADJUST R FOR IP=0.5A

FIG.4 - REPETITIVE PEAK ON-STATE

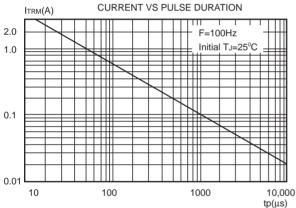
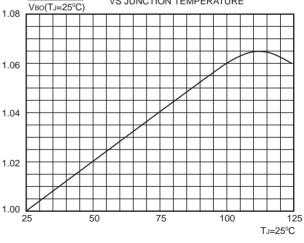
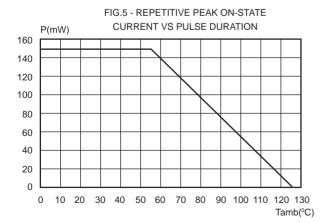


FIG.6 - NORMALIZED VBO CHANGE **VS JUNCTION TEMPERATURE** 





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