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November 2013

FDP42AN15A0

N-Channel PowerTrench[®] MOSFET 150 V, 35 A, 42 m Ω

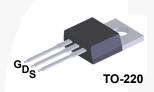
Features

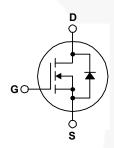
- $R_{DS(on)}$ = 36 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 12 A
- $Q_{G(tot)}$ = 33 nC (Typ.) @ V_{GS} = 10 V
- · Low Miller Charge
- Low Q_{rr} Body Diode
- UIS Capability (Single Pulse and Repetitive Pulse)

Formerly developmental type 82864

Applications

- · Consumer Appliances
- · Synchronous Rectification
- · Uninterruptible Power Supply
- · Micro Solar Inverter





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	FDP42AN15A0	Unit
V _{DSS}	Drain to Source Voltage	150	V
V _{GS}	Gate to Source Voltage	±20	V
	Drain Current		
	Continuous ($T_C = 25^{\circ}C$, $V_{GS} = 10V$)	35	Α
I_{D}	Continuous (T _C = 100°C, V _{GS} = 10V)	24	
	Continuous ($T_{amb} = 25^{\circ}C$, $V_{GS} = 10V$, with $R_{\theta JA} = 43^{\circ}C/W$)	5	А
	Pulsed	Figure 4	Α
E _{AS}	Single Pulse Avalanche Energy (Note 1)	90	mJ
	Power dissipation	150	W
P_{D}	Derate above 25°C	1.00	W/°C
T_J, T_{STG}	Operating and Storage Temperature	-55 to 175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62	°C/W

Package Marking and Ordering Information	Package	age Marking	and Ordering	Information
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Device Marking	Device	Package	Reel Size	Tape Width	Quantity	
FDP42AN15A0	FDP42AN15A0	TO-220	Tube	N/A	50 units	

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	ns	Min	Тур	Max	Unit
Off Characteristics			•			•	•
B _{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$		150	-	-	V
1	Zero Gate Voltage Drain Current	V _{DS} = 120V		-	-	1	
IDSS	Zero Gate voltage Drain Current	$V_{GS} = 0V$ $T_C =$: 150°C	-	-	250	μΑ
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20V$		-	-	±100	nA

On Characteristics

V _{GS(TH)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\mu A$	2	-	4	V
		$I_D = 12A, V_{GS} = 10V$	-	0.036	0.042	
r-accoun	Drain to Source On Resistance	$I_{D} = 6A, V_{GS} = 6V$	-	0.040	0.060	0
r _{DS(ON)} Drain to Source On Resistance	$I_D = 12A, V_{GS} = 10V,$ $T_J = 175$ °C	-	0.090	0.107	. 52	

Dynamic Characteristics

C _{ISS}	Input Capacitance	V 25V V 0V	-	2150	-	pF
Coss	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz	-	225	-	pF
C _{RSS}	Reverse Transfer Capacitance	1 = 1111112	-	45	-	pF
$Q_{g(TOT)}$	Total Gate Charge at 10V	$V_{GS} = 0V \text{ to } 10V$		30	39	nC
$Q_{g(TH)}$	Threshold Gate Charge	$V_{GS} = 0V \text{ to } 2V$ $V_{DD} = 75V$	-	4.2	5.4	nC
Q_{gs}	Gate to Source Gate Charge	I _D = 12A	-	9.5	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau	$I_g = 1.0 \text{mA}$	-	5.3	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	6.9	-	nC

Switching Characteristics $(V_{GS} = 10V)$

t _{ON}	Turn-On Time		-/	-	46	ns
t _{d(ON)}	Turn-On Delay Time		7	11	-	ns
t _r	Rise Time	V _{DD} = 75V, I _D = 12A	-	19	-,,	ns
t _{d(OFF)}	Turn-Off Delay Time	$V_{GS} = 10V, R_{GS} = 7.5\Omega$	-	27	-	ns
t _f	Fall Time		-	23	- ·	ns
t _{OFF}	Turn-Off Time		-	-	74	ns

Drain-Source Diode Characteristics

\/	Source to Drain Diode Voltage	I _{SD} = 12A	-	/	1.25	V
V_{SD}	Source to Drain Diode voltage	I _{SD} = 6A	-	-	1.0	V
t _{rr}	Reverse Recovery Time	$I_{SD} = 12A$, $dI_{SD}/dt = 100A/\mu s$	-	-	82	ns
Q _{RR}	Reverse Recovered Charge	$I_{SD} = 12A$, $dI_{SD}/dt = 100A/\mu s$	-	-	204	nC

Notes: 1: Starting $T_J = 25$ °C, L = 0.2mH, $I_{AS} = 30$ A.

150

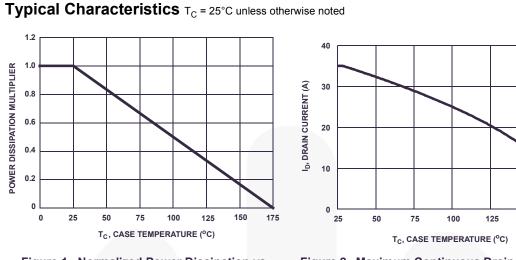


Figure 1. Normalized Power Dissipation vs Ambient Temperature

Figure 2. Maximum Continuous Drain Current vs Case Temperature

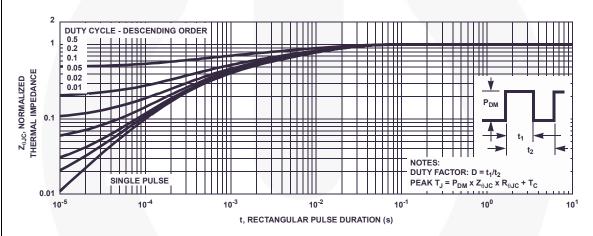


Figure 3. Normalized Maximum Transient Thermal Impedance

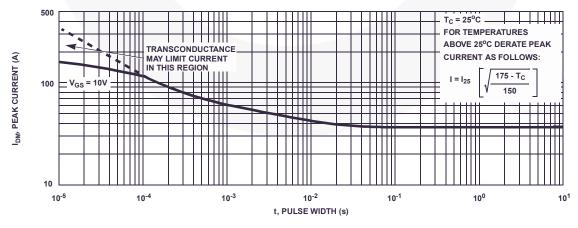
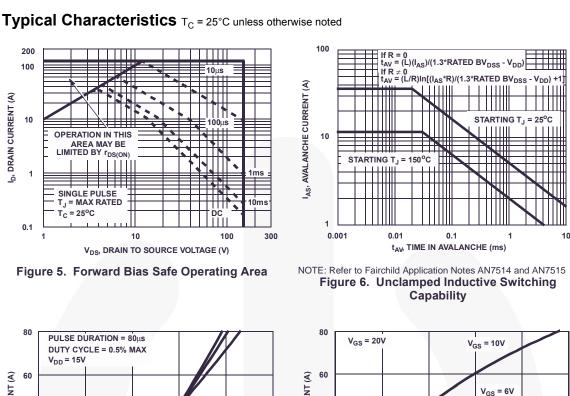


Figure 4. Peak Current Capability



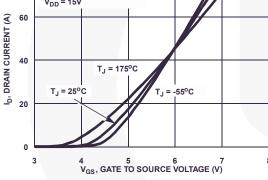


Figure 7. Transfer Characteristics

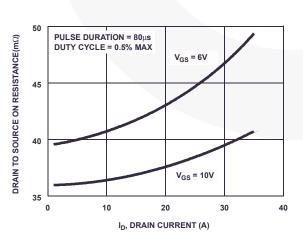


Figure 9. Drain to Source On Resistance vs Drain Current

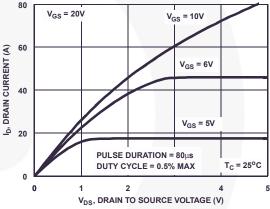


Figure 8. Saturation Characteristics

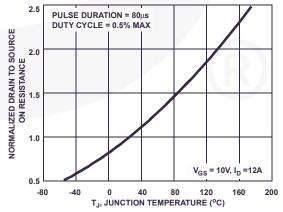


Figure 10. Normalized Drain to Source On Resistance vs Junction Temperature

Typical Characteristics T_C = 25°C unless otherwise noted

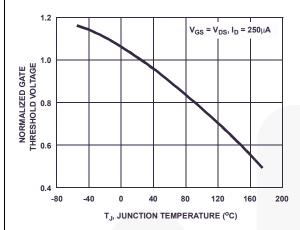


Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature

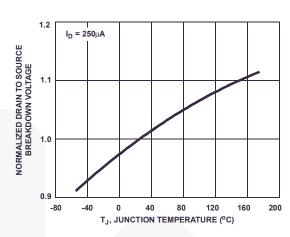


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

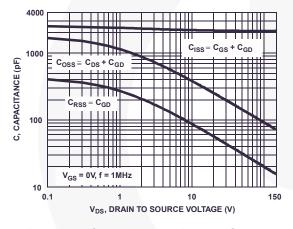


Figure 13. Capacitance vs Drain to Source Voltage

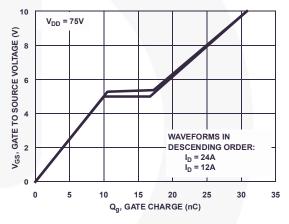


Figure 14. Gate Charge Waveforms for Constant Gate Current

Test Circuits and Waveforms

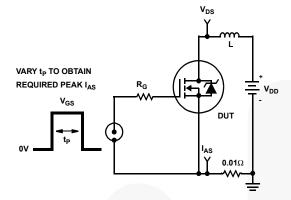


Figure 15. Unclamped Energy Test Circuit

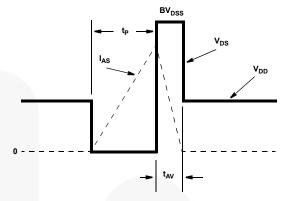


Figure 16. Unclamped Energy Waveforms

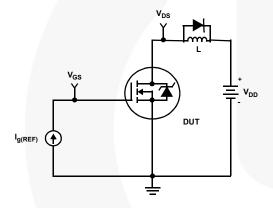


Figure 17. Gate Charge Test Circuit

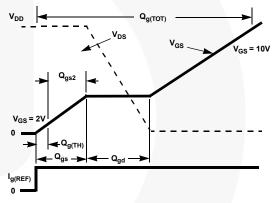


Figure 18. Gate Charge Waveforms

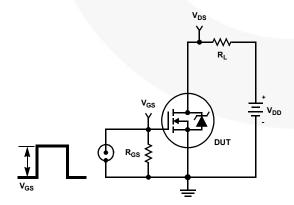


Figure 19. Switching Time Test Circuit

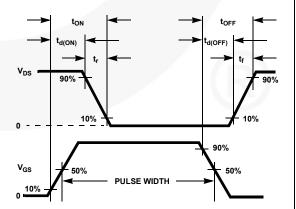
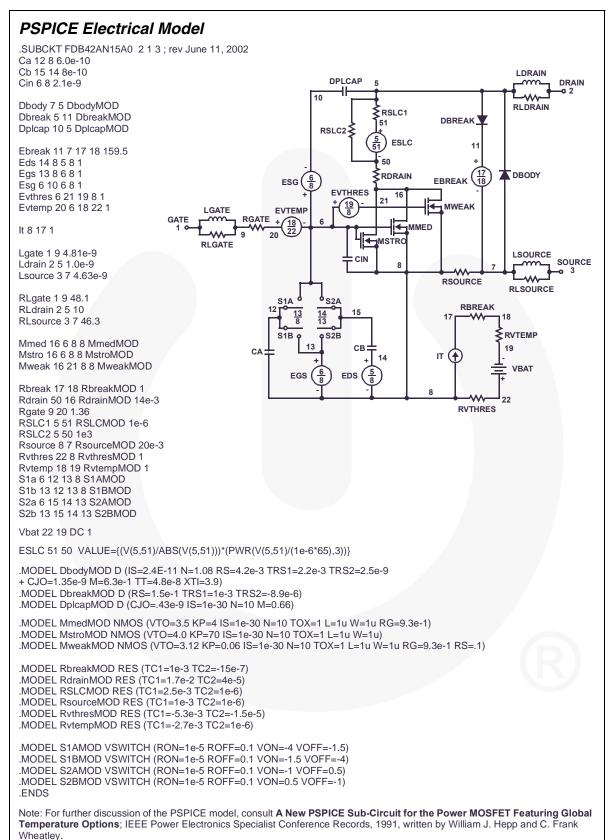


Figure 20. Switching Time Waveforms



SABER Electrical Model rev June 11, 2002 template FDB42AN15A0 n2,n1,n3 electrical n2,n1,n3 var i iscl dp..model dbodymod = (isl=2.4e-11,nl=1.08,rs=4.2e-3,trs1=2.2e-3,trs2=2.5e-9,cjo=1.35e-9,m=6.3e-1,tt=4.8e-8,xti=3.9) dp..model dbreakmod = (rs=1.5e-1,trs1=1e-3,trs2=-8.9e-6) dp..model dplcapmod = (cjo=.43e-9,isl=10e-30,nl=10,m=0.66) m..model mmedmod = (type=_n,vto=3.5,kp=4,is=1e-30, tox=1) m..model mstrongmod = (type=_n,vto=4.0,kp=70,is=1e-30, tox=1) m..model mweakmod = (type=_n,vto=3.12,kp=0.06,is=1e-30, tox=1,rs=.1) I DRAIN sw_vcsp..model s1amod = (ron=1e-5,roff=0.1,von=-4,voff=-1.5) DPLCAP DRAIN sw_vcsp..model s1bmod = (ron=1e-5,roff=0.1,von=-1.5,voff=-4) 10 sw_vcsp..model s2amod = (ron=1e-5,roff=0.1,von=-1,voff=0.5) RLDRAIN sw_vcsp..model s2bmod = (ron=1e-5,roff=0.1,von=0.5,voff=-1) ₹RSLC1 c.ca n12 n8 = 6.0e-1051 RSLC2 ₹ c.cb n15 n14 = 8e-10 ISCL c.cin n6 n8 = 2.1e-9DBREAK 50 dp.dbody n7 n5 = model=dbodymod RDRAIN dp.dbreak n5 n11 = model=dbreakmod 8 **ESG** dp.dplcap n10 n5 = model=dplcapmod DBODY **EVTHRES** 21 MWFAK spe.ebreak n11 n7 n17 n18 = 159.5 GATE LGATE **EVTEMP RGATE** MMED EBREAK spe.eds n14 n8 n5 n8 = 1 Ιg 20 spe.egs n13 n8 n6 n8 = 1 MSTR RLGATE spe.esq n6 n10 n6 n8 = 1 LSOURCE spe.evthres n6 n21 n19 n8 = 1CIN SOURCE spe.evtemp n20 n6 n18 n22 = 1 RSOURCE RLSOURCE i.it n8 n17 = 1**RBREAK** I.lgate n1 n9 = 4.81e-917 I.Idrain n2 n5 = 1.0e-9RVTEMP I.lsource n3 n7 = 4.63e-9CR 19 CA IT 14 res.rlgate n1 n9 = 48.1 VRAT res.rldrain n2 n5 = 10 **EGS** FDS res.rlsource n3 n7 = 46.3 m.mmed n16 n6 n8 n8 = model=mmedmod, l=1u, w=1u **RVTHRES** m.mstrong n16 n6 n8 n8 = model=mstrongmod, l=1u, w=1u m.mweak n16 n21 n8 n8 = model=mweakmod, l=1u, w=1u res.rbreak n17 n18 = 1, tc1=1e-3,tc2=-15e-7 res.rdrain n50 n16 = 14e-3, tc1=1.7e-2,tc2=4e-5 res.rgate n9 n20 = 1.36 res.rslc1 n5 n51 = 1e-6, tc1=2.5e-3,tc2=1e-6 res.rslc2 n5 n50 = 1e3res.rsource n8 n7 = 20e-3, tc1=1e-3,tc2=1e-6 res.rvthres n22 n8 = 1, tc1=-5.3e-3,tc2=-1.5e-5 res.rvtemp n18 n19 = 1, tc1=-2.7e-3,tc2=1e-6 sw_vcsp.s1a n6 n12 n13 n8 = model=s1amod sw_vcsp.s1b n13 n12 n13 n8 = model=s1bmod sw_vcsp.s2a n6 n15 n14 n13 = model=s2amod sw_vcsp.s2b n13 n15 n14 n13 = model=s2bmod v.vbat n22 n19 = dc=1 equations { i (n51->n50) +=iscl iscl: v(n51,n50) = ((v(n5,n51)/(1e-9+abs(v(n5,n51))))*((abs(v(n5,n51)*1e6/65))**3)))

SPICE Thermal Model JUNCTION REV 23 June 11, 2002 FDB42AN15A0_Thermal CTHERM1 TH 6 2e-3 CTHERM2 6 5 4.5e-3 CTHERM3 5 4 7e-3 RTHERM1 CTHERM1 CTHERM4 4 3 3e-2 CTHERM5 3 2 4e-2 CTHERM6 2 TL 8.5e-1 RTHERM1 TH 6 6.2e-2 RTHERM2 6 5 8.2e-2 RTHERM3 5 4 9.2e-2 RTHERM2 CTHERM2 RTHERM4 4 3 9.7e-2 RTHERM5 3 2 0.2 RTHERM6 2 TL 0.22 5 SABER Thermal Model RTHERM3 CTHERM3 SABER thermal model FDB42AN15A0_Thermal template thermal_model th tl thermal_c th, tl ctherm.ctherm1 th 6 = 2e-3 ctherm.ctherm2 6 5 =4.5e-3 RTHERM4 CTHERM4 ctherm.ctherm3 5 4 =7e-3 ctherm.ctherm4 4 3 =3e-2 ctherm.ctherm5 3 2 =4e-2 ctherm.ctherm6 2 tl =8.5e-1 3 rtherm.rtherm1 th 6 =6.2e-2 rtherm.rtherm2 6 5 =8.2e-2 RTHERM5 CTHERM5 rtherm.rtherm3 5 4 = 9.2e-2 rtherm.rtherm4 4 3 =9.7e-2 rtherm.rtherm5 3 2 =0.2 rtherm.rtherm6 2 tl =0.22} 2 RTHERM6 CTHERM6 CASE

Mechanical Dimensions

TO-220 3L

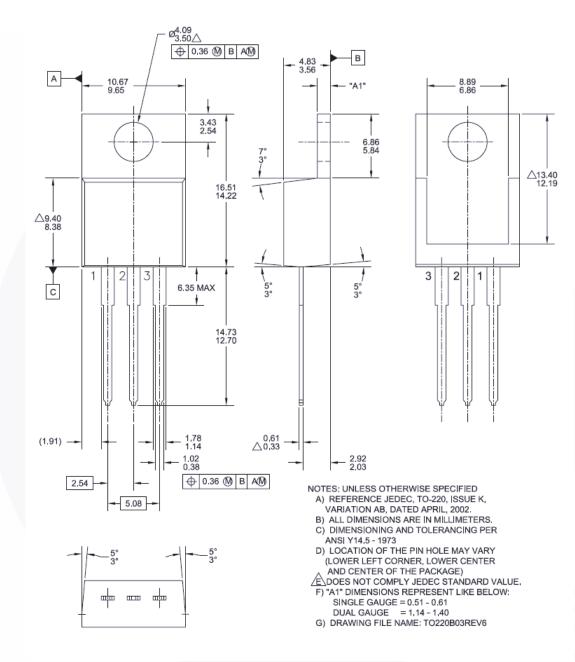


Figure 21. TO-220, Molded, 3Lead, Jedec Variation AB

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Dimension in Millimeters





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