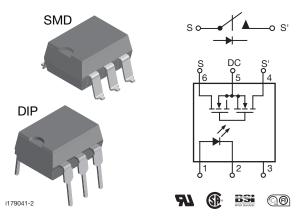


## LH1525AT, LH1525AAB, LH1525AABTR

**Vishay Semiconductors** 

# 1 Form A Solid-State Relay



### DESCRIPTION

The LH1525 relay are SPST normally open switches (1 form A) that can replace electromechanical relays in many applications. The relay requires a minimal amount of LED drive current to operate, making it ideal for battery powered and power consumption sensitive applications. The relav is constructed using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology, comprised of a photodiode array, switch-control circuitry, and MOSFET switches. In addition, the relay employs current-limiting circuitry, enabling it to pass lightning surge testing as per ANSI/TIA-968-B and other regulatory surge requirements when overvoltage protection is provided. The relay can be configured for AC/DC or DC-only operation.

### **FEATURES**

- Extremely low operating current
- High speed operation
- Isolation test voltage 5300 V<sub>RMS</sub>
- Current limit protection
- High surge capability
- DC only option
- Clean bounce free switching
- Low power consumption
- Surface mountable
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### **APPLICATIONS**

- · General telecom switching
- Battery powered switch applications
- Industrial controls
- Programmable controllers
- Instrumentation

### Note

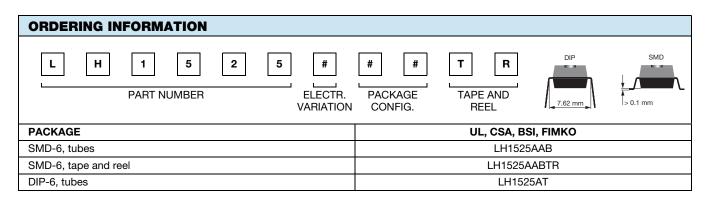
See "solid-state relays" (application note 56)

### AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection CSA: certification 093751

BSI: no. 7979/7980

FIMKO: 25419



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Document Number: 83823

RoHS



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
LED input ratings: continuous forward current		I <sub>F</sub>	50	mA		
LED input ratings: reverse voltage		V <sub>R</sub>	8	V		
OUTPUT						
Output operation (each channel): DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	400	V		
Continuous DC load current, bidirectional operation pin 4 to 6		ار	125	mA		
Continuous DC load current, unidirectional operation pins 4, 6 (+) to pin 5 (-)		ار	250	mA		
SSR						
Ambient operating temperature range		T <sub>amb</sub>	- 40 to + 85	°C		
Storage temperature range		T <sub>stg</sub>	- 40 to + 150	°C		
Pin soldering temperature <sup>(1)</sup>	t = 10 s max.	T <sub>sld</sub>	260	°C		
Input to output isolation test voltage	t = 1 s	V <sub>ISO</sub>	5300 V <sub>RI</sub>			
Power dissipation		P <sub>diss</sub>	550	mW		

### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

(1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT					•	
LED forward current, switch turn-on	I <sub>L</sub> = 100 mA, t = 10 ms	I <sub>Fon</sub>		0.33	0.5	mA
LED forward current, switch turn-off	$V_L = \pm 350 \text{ V}, \text{ t} = 100 \text{ ms}$	I <sub>Foff</sub>	0.001	0.23		mA
LED forward voltage	I <sub>F</sub> = 1.5 mA	V <sub>F</sub>	0.8	1.16	1.40	V
OUTPUT			•		•	•
On-resistance, AC/DC, each pole	$I_F = 1.5 \text{ mA}, I_L = \pm 50 \text{ mA}$	R <sub>ON</sub>	17	26	36	Ω
On-resistance, DC: pin 4, 6 (+) to 5 (-)	I <sub>F</sub> = 1.5 mA, I <sub>L</sub> = 100 mA	R <sub>ON</sub>	4.25	7	8.25	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R <sub>OFF</sub>		2000		GΩ
Current limit AC <sup>(1)</sup> : pin 4 (±) to 6 (±)	$I_F = 1.5 \text{ mA}, t = 5 \text{ ms}, V_L = 7 \text{ V}$	I <sub>LMT</sub>	170	185	270	mA
Off-state leakage current	$I_{F} = 0 \text{ mA}, V_{L} = \pm 100 \text{ V}$	Ι <sub>Ο</sub>		0.67	200	nA
	$I_F = 0 \text{ mA}, V_L = \pm 400 \text{ V}$	Ι <sub>Ο</sub>		0.096	1	μA
Output capacitance	$I_{\rm F} = 0  {\rm mA},  V_{\rm L} = 1  {\rm V}$	Co		22		pF
	$I_{\rm F} = 0  {\rm mA},  {\rm V_L} = 50  {\rm V}$	Co		6.42		pF
Switch offset	$I_F = 5 \text{ mA}$	V <sub>OS</sub>		0.2		μV
TRANSFER	•	•	•	•	•	•
Capacitance (input to output)	V <sub>ISO</sub> = 1 V	C <sub>IO</sub>		0.75		pF

#### Notes

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

<sup>(1)</sup> No DC mode current limit available.

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I <sub>F</sub> = 1.5 mA, I <sub>L</sub> = 50 mA	t <sub>on</sub>		1.25		ms
	I <sub>F</sub> = 5 mA, I <sub>L</sub> = 50 mA	t <sub>on</sub>		0.22	1	ms
Turn-off time	l <sub>F</sub> = 1.5 mA, l <sub>L</sub> = 50 mA	t <sub>off</sub>		0.6		ms
	I <sub>F</sub> = 5 mA, I <sub>L</sub> = 50 mA	t <sub>off</sub>		1.1	1.5	ms

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SAFETY AND INSU	LATION RATIN	GS			
PARAMETER		TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification		IEC 68 part 1		40/85/21	
Pollution degree		DIN VDE 0109		2	
Tracking resistance (comparative tracking index)		Insulation group Illa	CTI	175	
Highest allowable overvoltage		Transient overvoltage	VIOTM	8000	V <sub>peak</sub>
Max. working insulation voltage		Recurring peak voltage	VIORM	890	V <sub>peak</sub>
Insulation resistance at 25 °C			R <sub>IS</sub>	≥ 10 <sup>12</sup>	Ω
Insulation resistance at T <sub>S</sub> Insulation resistance at 100 °C		V <sub>IO</sub> = 500 V	R <sub>IS</sub>	≥ 10 <sup>9</sup>	Ω
			R <sub>IS</sub>	≥ 10 <sup>11</sup>	Ω
Partial discharge test voltage	je	Methode a, V <sub>pd</sub> = V <sub>IORM</sub> x 1.875	V <sub>pd</sub>	1669	V <sub>peak</sub>
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T <sub>SI</sub>	175	°C
	Input current		I <sub>SI</sub>	300	mA
	Output power		P <sub>SO</sub>	700	mW
Minimum external air gap (c	clearance)	Measured from input terminals to output terminals, shortest distance through air	≥7		mm
Minimum external tracking (creepage)		Measured from input terminals to output terminals, shortest distance path along body		≥ 7	mm

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

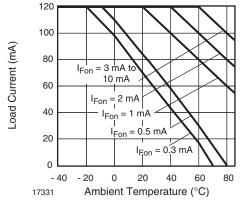


Fig. 1 - Recommended Operating Conditions

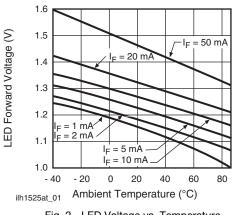
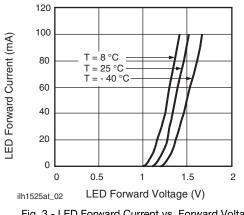
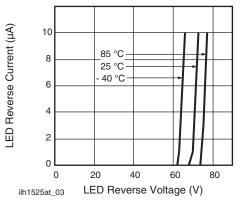


Fig. 2 - LED Voltage vs. Temperature









3 For technical questions, contact: optocoupleranswers@vishay.com Document Number: 83823

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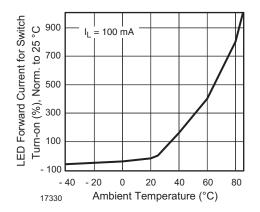


Fig. 5 - LED Current for Switch Turn-on vs. Temperature

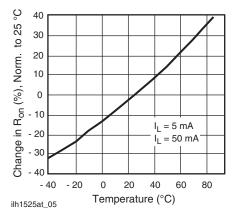


Fig. 6 - On-Resistance vs. Temperature

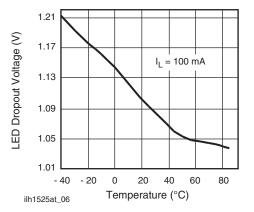


Fig. 7 - LED Dropout Voltage vs. Temperature

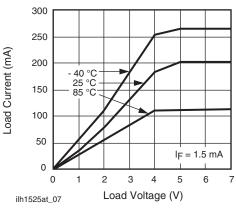


Fig. 8 - Load Current vs. Load Voltage

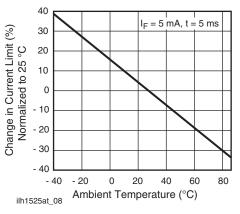


Fig. 9 - Current Limit vs. Temperature

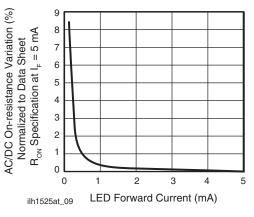


Fig. 10 - Variation in On-resistance vs. LED Current

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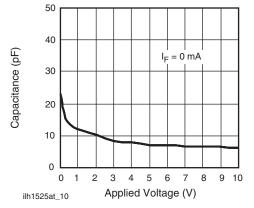
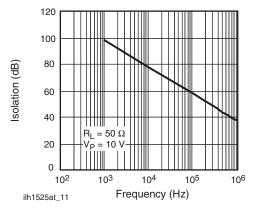
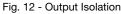
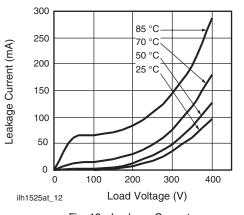
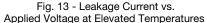


Fig. 11 - Switch Capacitance vs. Applied Voltage









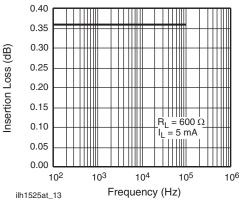


Fig. 14 - Insertion Loss vs. Frequency

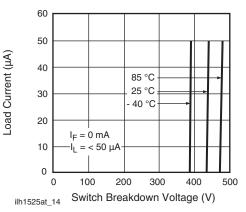


Fig. 15 - Switch Breakdown Voltage vs. Load Current

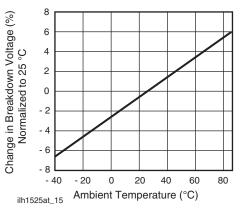


Fig. 16 - Switch Breakdown Voltage vs. Temperature





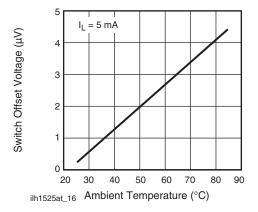


Fig. 17 - Switch Offset Voltage vs. Temperature

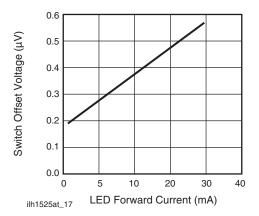


Fig. 18 - LED Offset Voltage vs. LED Current

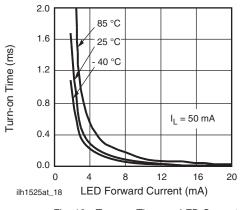


Fig. 19 - Turn-on Time vs. LED Current

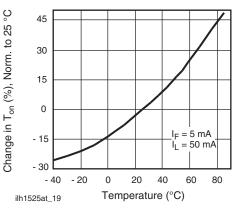


Fig. 20 - Turn-off Time vs. Temperature

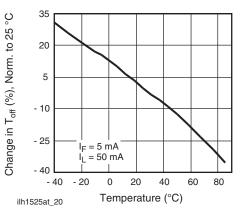


Fig. 21 - Turn-on Time vs. LED Temperature

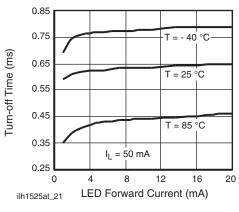


Fig. 22 - Turn-off Time vs. LED Current

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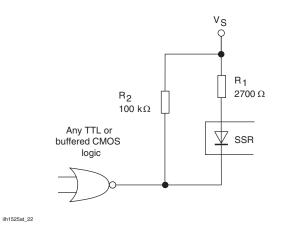
## APPLICATIONS

### INPUT CONTROL

The LH1525 low turn-on current SSR has highly sensitive photodetection circuits that will detect even the most minute currents flowing through the LED. Leakage current must be considered when designing a circuit to turn on and off these relays.

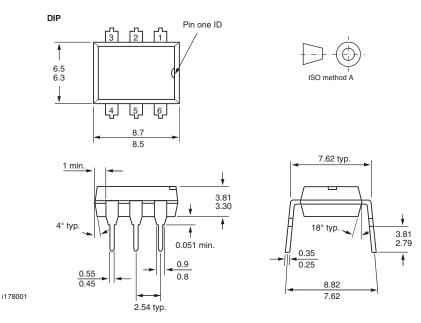
Figure 23 shows a typical logic circuit for providing LED drive current. R<sub>1</sub> is the input resistor that limits the amount of current flowing through the LED. For 5 V operation, a 2700  $\Omega$  resistor will limit the drive current to about 1.4 mA. Where high-speed actuation is desirable, use a lower value resistor for R<sub>1</sub>. An additional RC peaking circuit is not required with the LH1525 relay.

 $R_2$  is an optional pull-up resistor which pulls the logic level high output (V<sub>OH</sub>) up toward the VS potential. The pull-up resistance is set at a high value to minimize the overall current drawn from the VS. The primary purpose of this resistor is to keep the differential voltage across the LED below its turn-on threshold. LED dropout voltage is graphed vs. temperature in the typical performance characteristics section. When the logic gate is high, leakage current will flow through  $R_2$ .  $R_2$  will draw up to 8 mA before developing a voltage potential which may possibly turn on the LED. Each application should be evaluated, over the full operating temperature range to make sure that leakage current through the input control LED is kept to a value less than the minimum LED forward current for switch turn-off specification.





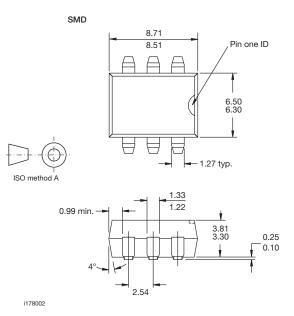
### **PACKAGE DIMENSIONS** in millimeters

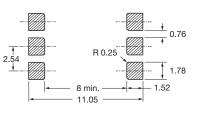


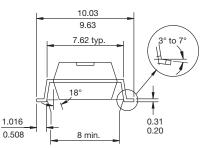


# LH1525AT, LH1525AAB, LH1525AABTR

**Vishay Semiconductors** 







## **PACKAGE MARKING**



### Note

• Tape and reel suffix (TR) is not part of the package marking.



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