

**DISPLAY Elektronik GmbH**

# DATA SHEET

**LCD MODULE**

**DEM 1024600M2 VMH-PW-N**

*Product Specification*

*Version: 1*

**28.04.2020**



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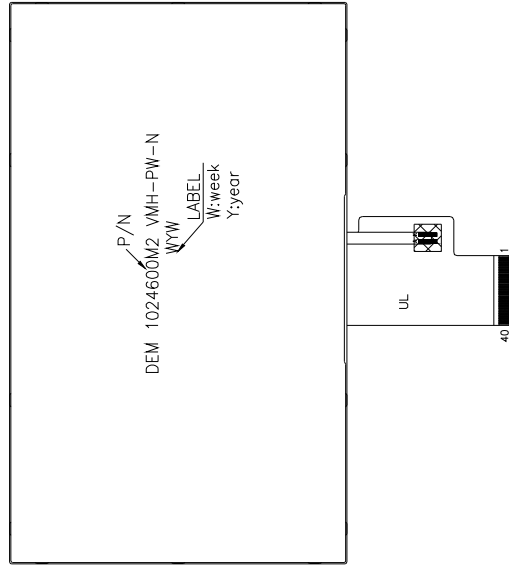
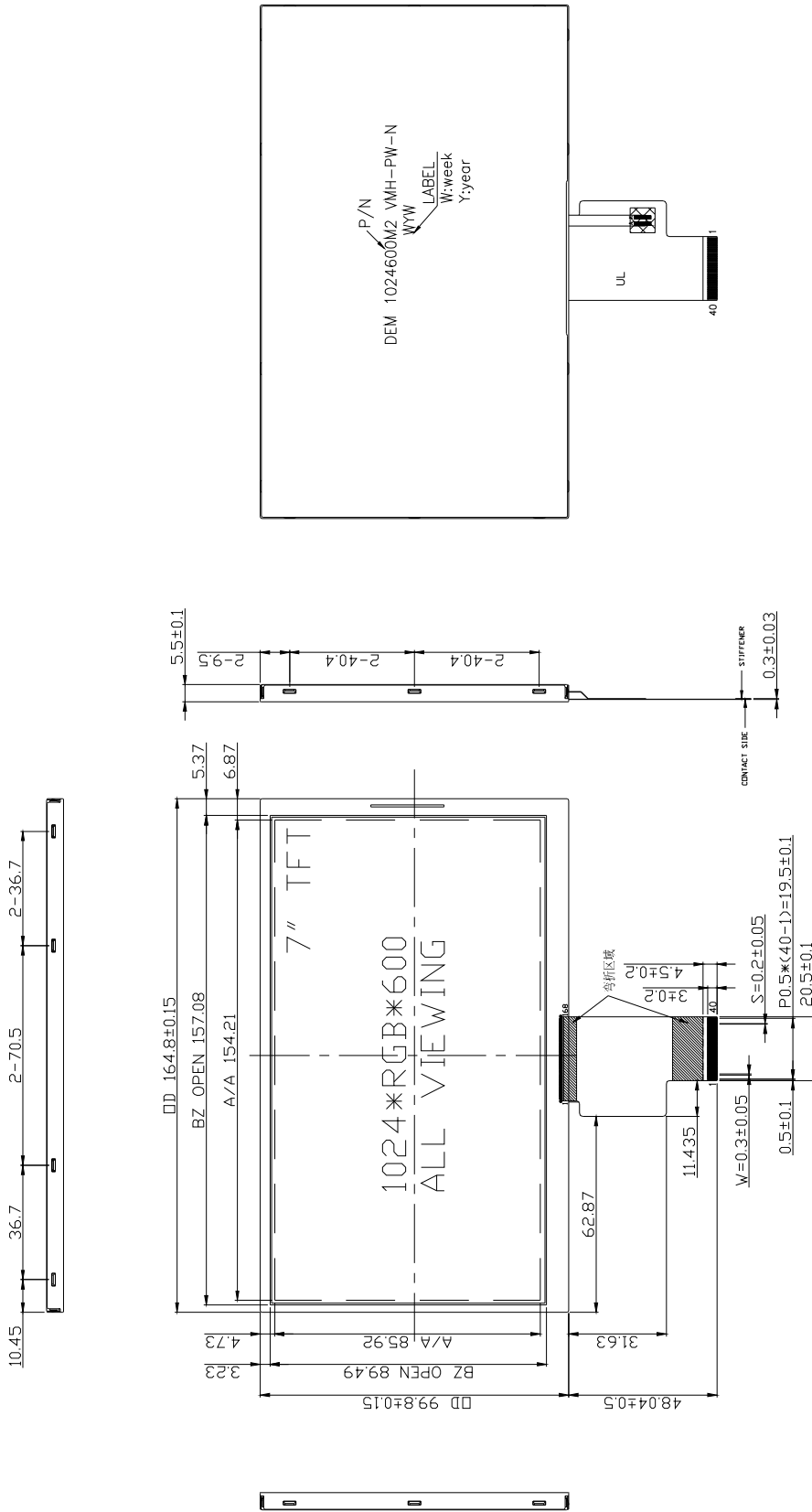
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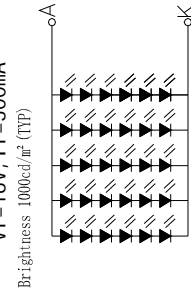
**1. GENERAL SPECIFICATIONS**

<b>ITEM</b>	<b>STANDARD VALUE</b>	<b>UNIT</b>
LCD TYPE	TFT/IPS/ NORMALLY BLACK/TRANSMISSIVE	
MODULE SIZE	164.80 x 99.80 x 5.50	mm
ACTIVE AREA	154.21 x 85.92	mm
PIXEL PITCH	0.1506 x 0.1432	
NUMBER OF PIXELS	1024 x 3(RGB) x 600	
DIVER IC	EK73215 + EK79001H	
INTERFACE TYPE	LVDS	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	-	O'CLOCK
COLORS	16.7 MILLION	
BACKLIGHT TYPE	30-DIES WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

2. EXTERNAL DIMENSIONS



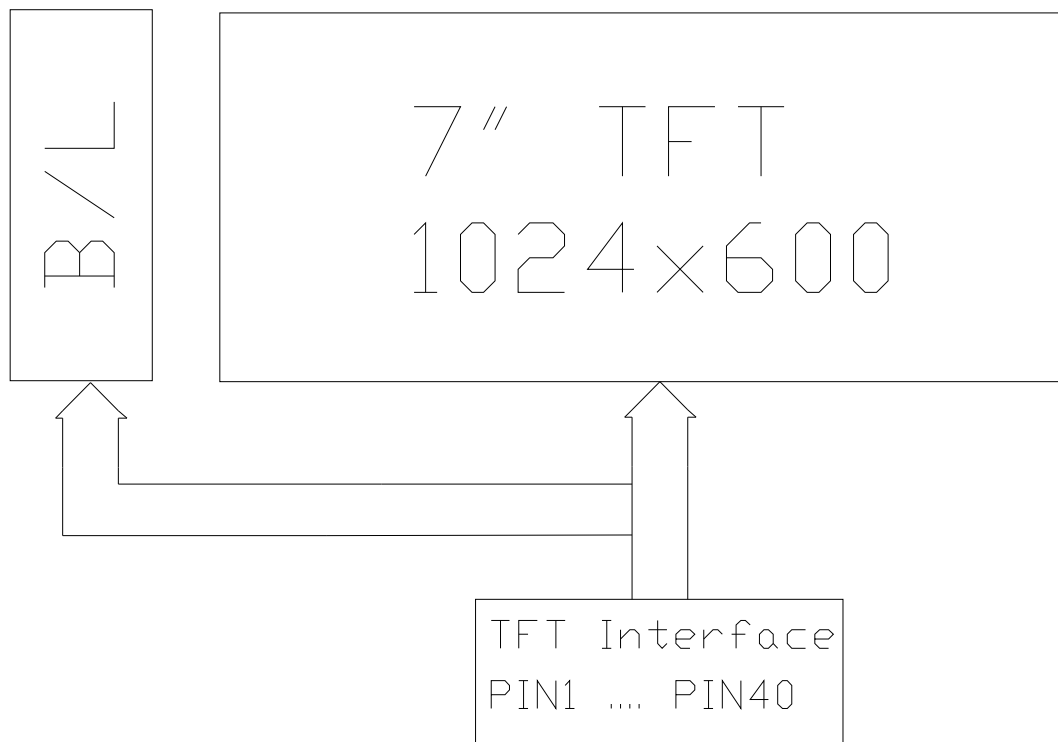
Circuit Diagram(LED SMT 6\*5=30 PCS), Color: WHITE  
 $V_f = 18V, I_f = 300mA$



Remark:

1. Unmarked tolerance is  $\pm 0.3$
2. All materials comply with RoHS

**3. BLOCK DIAGRAM**



## 4. PIN ASSIGNMENT

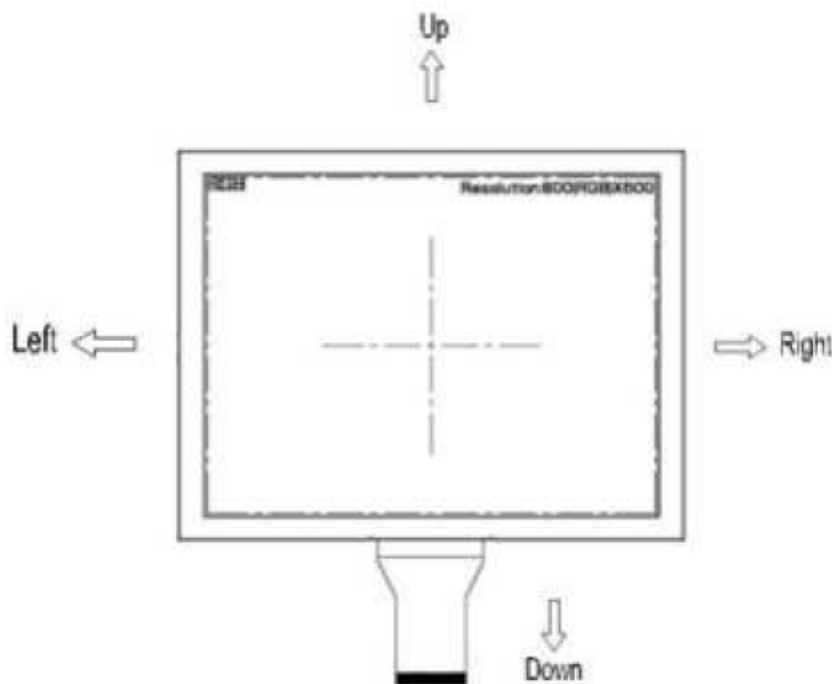
PIN NO.	SYMBOL	DESCRIPTION
1	VCOM	Common Voltage
2	VDD	Power supply
3	VDD	Power supply
4	NC	Not connection
5	RESET	Reset pin
6	STBYB	Standby mode, normally pulled high. STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
7	GND	Power ground
8	RXIN0-	Data lane0 input
9	RXIN0+	Data lane0 input
10	GND	Power ground
11	RXIN1-	Data lane1 input
12	RXIN1+	Data lane1 input
13	GND	Power ground
14	RXIN2-	Data lane2 input
15	RXIN2+	Data lane2 input
16	GND	Power ground
17	RXCLKIN-	CLK input
18	RXCLKIN+	CLK input
19	GND	Power ground
20	RXIN3-	Data lane3 input
21	RXIN3+	Data lane3 input
22	GND	Power ground
23	NC	Not connection
24	NC	Not connection
25	GND	Power ground
26	NC	Not connection
27	NC	Not connection
28	SELB	In LVDS interface connected HSD to FPC for Pin Setting SELB="L":8 bit SELB="H":6 bit
29	AVDD	Power for Analog Circuit
30	GND	Power ground
31	LED-	LED Cathode

32	LED-	LED Cathode
33	L/R	Source Right or Left sequence control. Normally pull high. L/R = "L", shift left: last data = S1←S2←S3.....←S1200 = first data. L/R = "H", shift right: first data = S1→S2→S3.....→S1200 = last data
34	U/D	Gate Up or Down scan control. Normally pull low. UPDN = "L", STV2 output vertical start pulse and UD pin output logical "0" to Gate driver.(Default) UPDN = "H", STV1 output vertical start pulse and UD pin output logical "1" to Gate driver.
35	VGL	Gate OFF Voltage
36	NC	Not connection
37	NC	Not connection
38	VGH	Gate ON Voltage
39	LED+	LED Anode
40	LED+	LED Anode

**SELECTION OF SCANNING MODE**

SETTING OF SCAN CONTROL INPUT		SCANNING DIRECTION
U/D	L/R	
GND	DVDD	UP TO DOWN, RIGHT TO LEFT
DVDD	GND	DOWN TO UP, LEFT TO RIGHT
GND	GND	UP TO DOWN, LEFT TO RIGHT
DVDD	DVDD	DOWN TO UP, RIGHT TO LEFT

DEFINITION OF SCANNING DIRECTION REFER TO THE FIGURE AS BELOW:

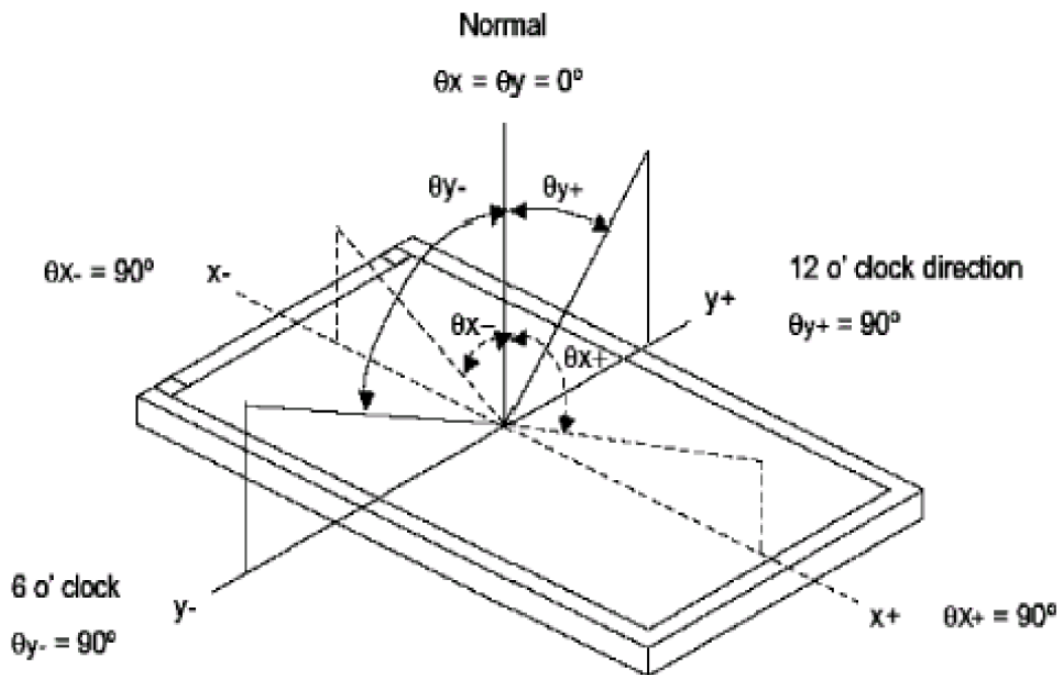




**5. OPTICAL CHARACTERISTICS**

ITEM	SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
			MIN	TYP.	MAX		
Luminance	L		800	1000	-	Cd/m <sup>2</sup>	
Contrast Ratio	CR	$\theta = 0^\circ$	640	800			
Response Time	Rising	T <sub>R</sub>	25°C	4	8	ms	
	Falling	T <sub>F</sub>		12	24		
CIE COLOUR COORDINATE	RED	XR	VIEWING NORMAL ANGLE				
		YR					
	GREEN	XG					
		YG					
	BLUE	XB					
		YB					
	WHITE	XW					
		YW					
VIEWING ANGLE	Hor.	$\theta_{x+}$	CR ≥ 10	80	85	Degree	
		$\theta_{x-}$		80	85		
	Ver.	$\theta_{y+}$		80	85		
		$\theta_{y-}$		80	85		

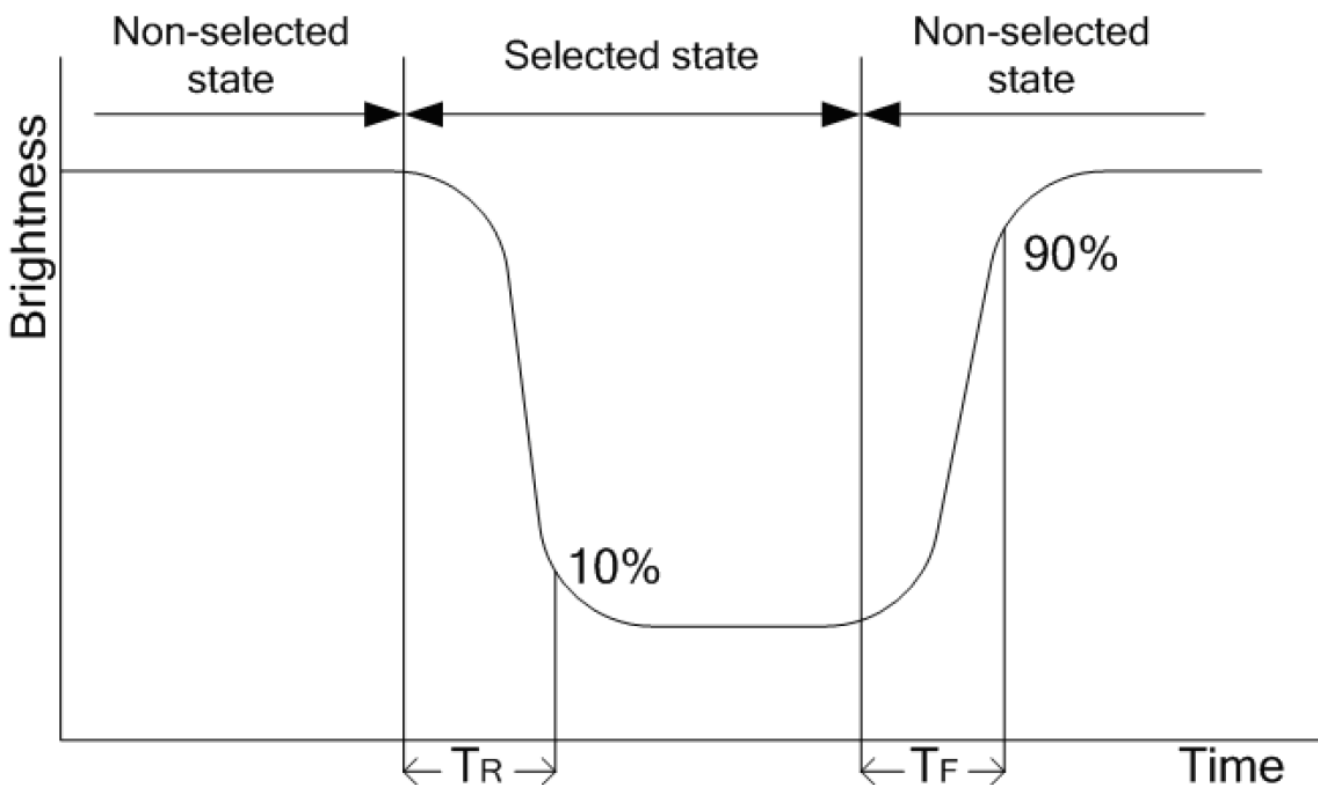
Note 1 : Definition of Viewing Angle  $\theta_x$  and  $\theta_y$  :



Note 2: Definition of contrast ratio CR:

$$CR = \frac{\text{Brightness of non-selected dots (white)}}{\text{Brightness of selected dots (black)}}$$

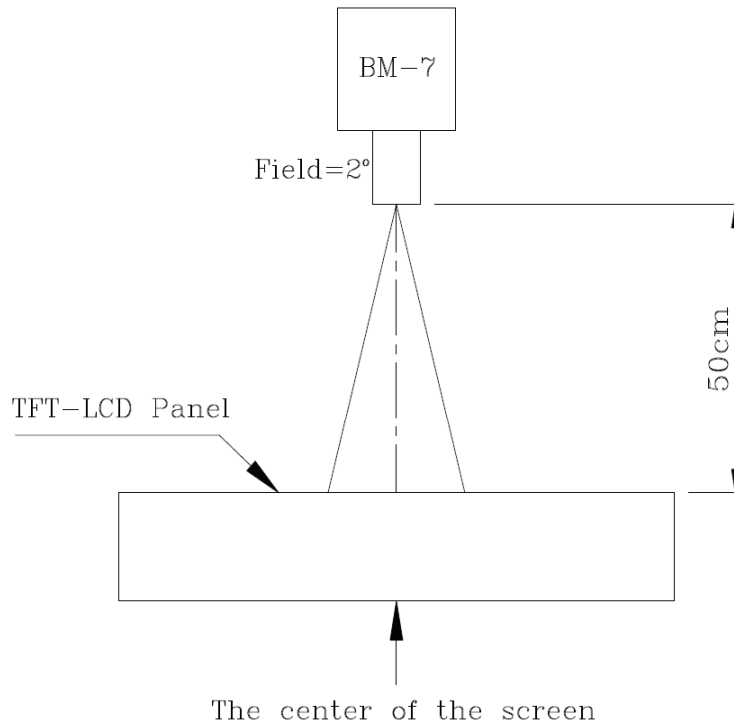
Note 3: Definition of response time ( $T_R$ ,  $T_F$ )



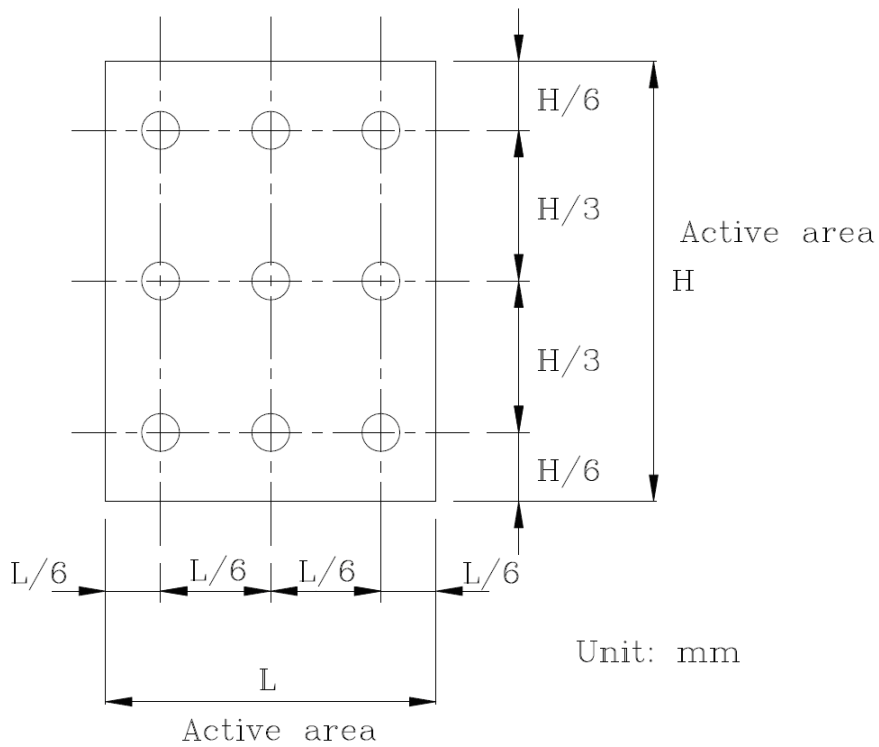
**Note 4: Definition of Luminance**

**①The Brightness Test Equipment Setup**

Field=2° (As measuring “black” image, field=2° is the best testing condition)



**②The Brightness Test Point Setup**



**6. ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	MIN	MAX	UNIT
Power Supply Voltage	VDD	-0.5	5.0	V
Operating Temperature	Top	-20	+70	°C
Storage Temperature	Tst	-30	+80	°C

**7. ELECTRICAL CHARACTERISTICS****7.1 BACKLIGHT DRIVING CONDITIONS**

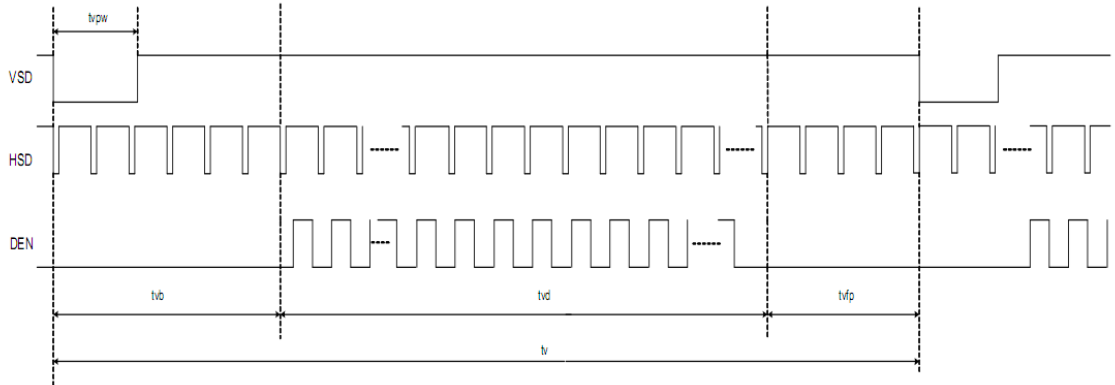
ITEM	SYMBOL	SPECIFICATIONS			UNIT	REMARK
		MIN	TYP.	MAX		
Supply Voltage	Vf		18		V	
Supply Current	IL		300		mA	
Power Consumption	P		5.4		W	
LED Lifetime			50,000		H <sub>r</sub>	

**7.2 ELECTRICAL CHARACTERISTICS**

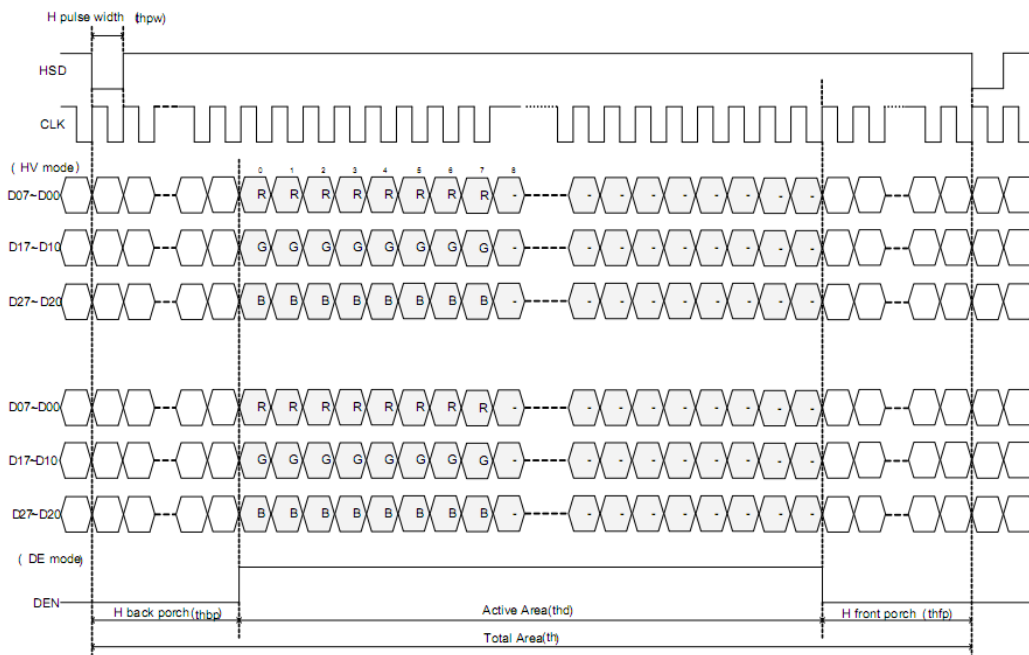
ITEM	SYMBOL	MIN	TYP.	MAX	UNIT
Power Supply	VDD	3.0	3.3	3.6	V
	AVDD	8.9	9.0	9.1	V
	VGH	17	18	19	V
	VGL	-6.5	-6.0	-5.5	V
	VCOM	3.0	3.15	3.3	V
Input Voltage	Vil	GND	-	0.3VDD	V
	Vih	0.7VDD	-	VDD	V

7.3 TIMING CHARACTERISTICS

7.3.1 Vertical input timing



7.3.2 Horizontal input timing



DE mode

DE mode					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

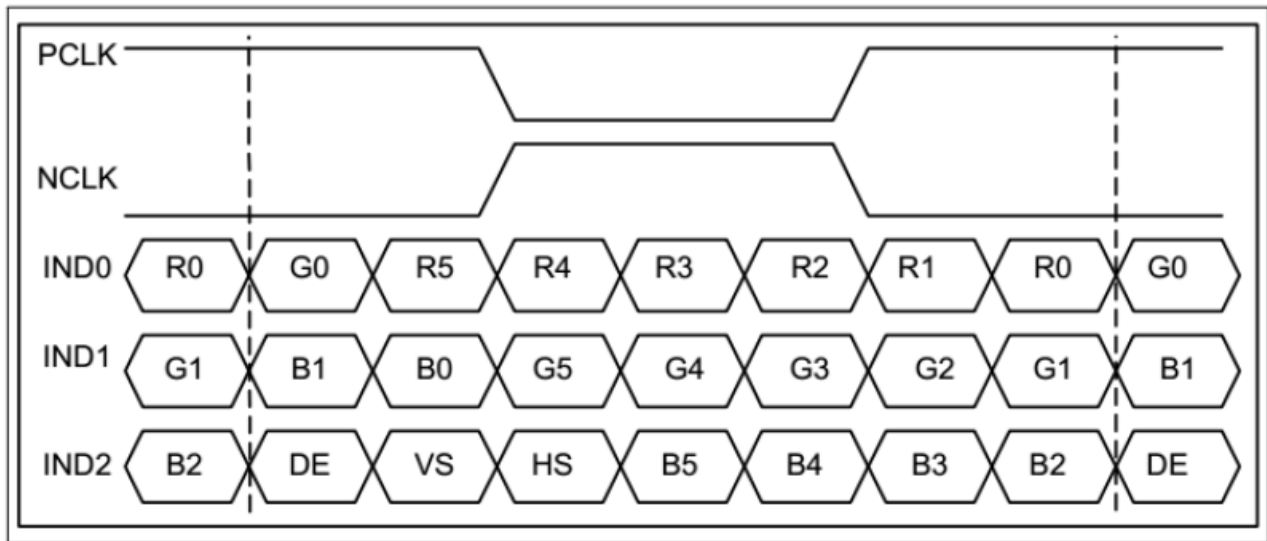
HV mode(1)

HV mode					
Horizontal input timing					
Parameter	Symbol	Value			Unit
Horizontal display area	thd	1024			DCLK
DCLK frequency@ Frame rate=60hz	fclk	Min.	Typ.	Max.	Mhz
		44.9	51.2	63	
1 Horizontal Line	th	1200	1344	1400	DCLK
HSYNC pulse width	thpw	Min.	1		
		Typ.	-		
		Max.	140		
HSYNC back porch	thbp	160	160	160	
HSYNC front porch	thfp	16	160	216	

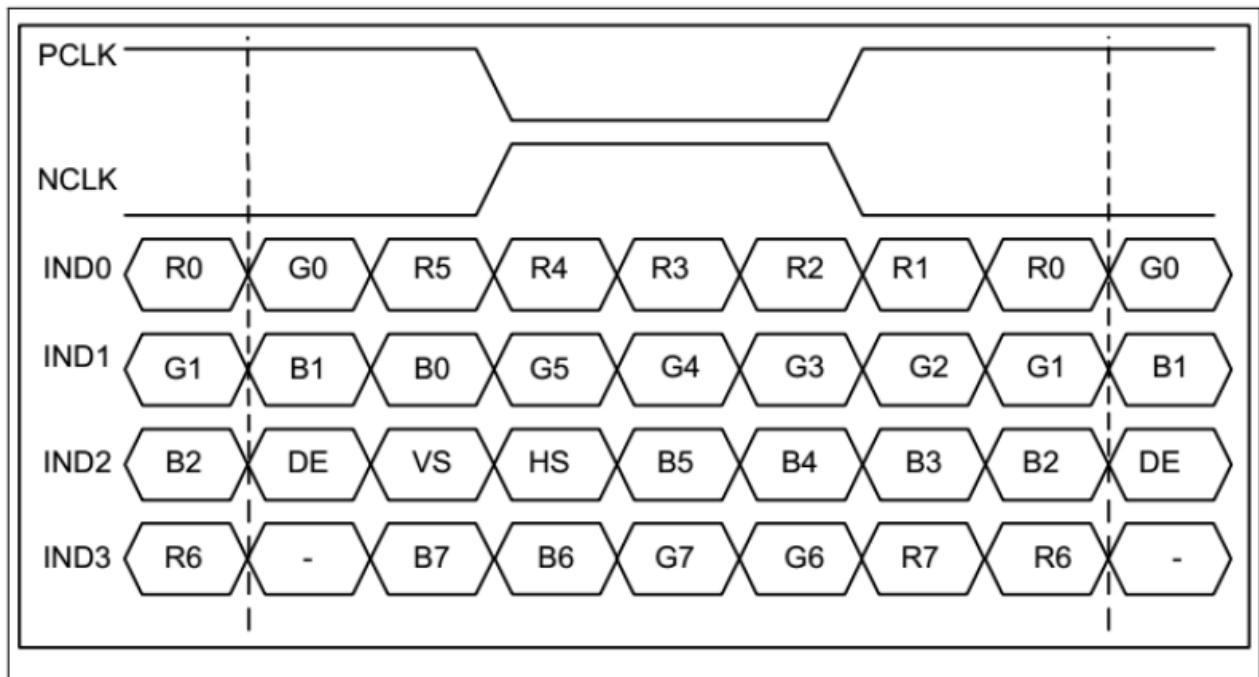
HV mode(2)

Vertical input timing					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	600			H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tpw	1	-	20	H
VSYNC back porch	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

DATE INPUT FORMAT



6-bit LVDS Input Timing chart



8-bit LVDS Input Timing chart

**8. RELIABILITY TEST**

<b>NO.</b>	<b>TEST ITEM</b>	<b>CONDITIONS</b>	
1	HIGH TEMPERATURE STORAGE	TA=80°C	96 Hr
2	LOW TEMPERATURE STORAGE	TA=-30°C	96 Hr
3	HIGH TEMPERATURE OPERATION	TA=70°C	96 Hr
4	LOW TEMPERATURE OPERATION	TA=-20°C	96 Hr
5	HIGH TEMPERATURE AND HIGH HUMIDITY OPERATION	+60°C, 90% RH	96 Hr
6	THERMAL SHOCK	-30°C → +80°C,	0.5 Hr: 5 CYCLES



**9. LCD MODULES HANDLING PRECAUTIONS**

- n** The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- n** If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- n** Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- n** The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- n** To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling the LCD module.
  - Tools required for assembly, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- n** Storage precautions  
When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C).Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

**10. OTHERS**

- n** Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- n** If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- n** To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
  - Exposed area of the printed circuit board
  - Terminal electrode sections.