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## November 2013

# FQP13N06L

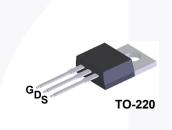
## N-Channel QFET<sup>®</sup> MOSFET 60 V, 13.6 A, 110 m $\Omega$

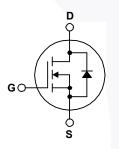
## Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

### Features

- 13.6 A, 60 V,  $R_{DS(on)}$  = 110 m $\Omega$  (Max.) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 6.8 A
- Low Gate Charge (Typ. 4.8 nC)
- Low Crss (Typ. 17 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





## Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

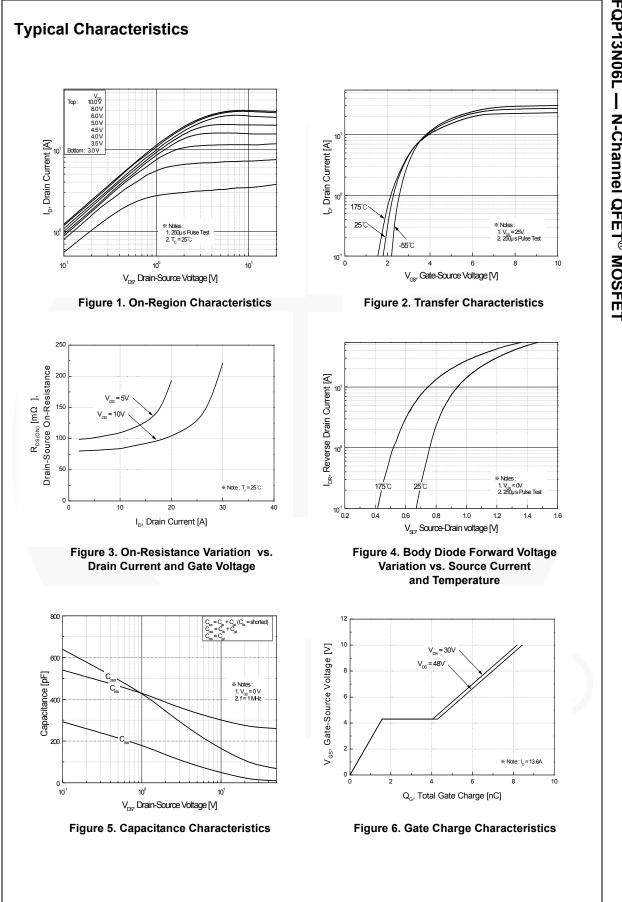
Symbol	Parameter		FQP13N06L	Unit
V <sub>DSS</sub>	Drain-Source Voltage		60	V
D	Drain Current - Continuous ( $T_C = 25^\circ$	C)	13.6	A
	- Continuous (T <sub>C</sub> = 100	°C)	9.6	A
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	54.4	A
V <sub>GSS</sub>	Gate-Source Voltage		± 20	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	90	mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	13.6	A
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	4.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns
P <sub>D</sub>	Power Dissipation ( $T_C = 25^{\circ}C$ )		45	W
	- Derate above 25°C		0.3	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

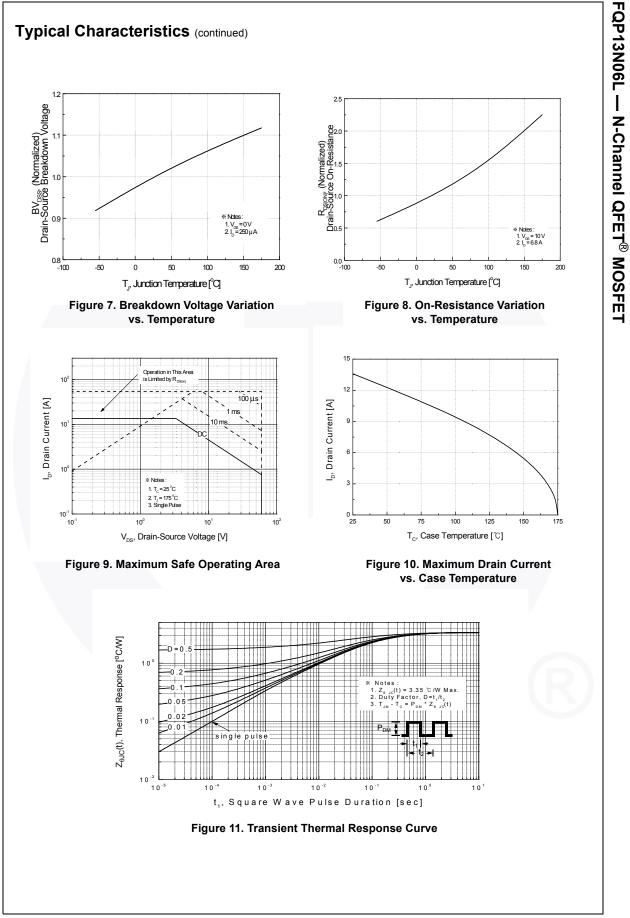
## **Thermal Characteristics**

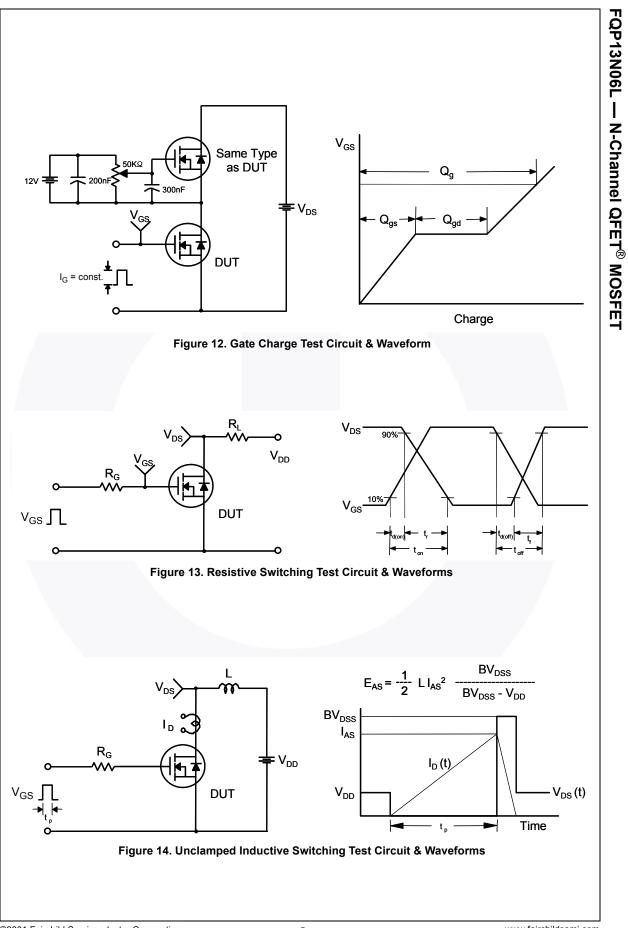
Symbol	Parameter	FQP13N06L	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	3.35	°C/W	
$R_{\thetaJA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

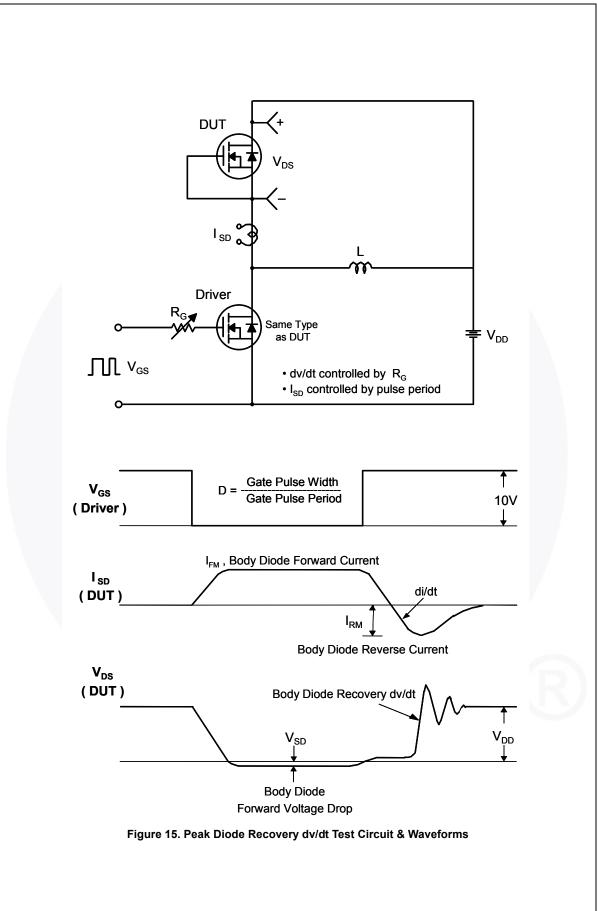
Part NumberTop MarkPackageFQP13N06LFQP13N06LTO-220		Package	e Packing Method R	Reel Size	Tape Width		h Q	Quantity	
		TO-220	Tube N/A		N/A		5	50 units	
lectri	cal Cl	haracteristics	T <sub>C</sub> = 25°C	unless otherwise noted.					
Symbol		Parameter		Test Conditi	ions	Min	Тур	Max	Unit
	rooto	victico							
Off Cha BV <sub>DSS</sub>	1		ane	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		60			V
$\Delta BV_{DSS}$	Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient				00			-	
$/ \Delta T_{J}$			ure	$I_D = 250 \ \mu A$ , Referenced to $25^{\circ}C$			0.05		V/°C
I <sub>DSS</sub>	Zoro (	Zana Osta Maltana Dasia Oranat		$V_{DS}$ = 60 V, $V_{GS}$ = 0				1	μA
	Zero Gate Voltage Drain Current		ent	V <sub>DS</sub> = 48 V, T <sub>C</sub> = 150°C				10	μA
I <sub>GSSF</sub>	Gate-E	Body Leakage Current,	Forward	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V				100	nA
I <sub>GSSR</sub>	Gate-E	Body Leakage Current,	Reverse	$V_{GS}$ = -20 V, $V_{DS}$ = 0	V			-100	nA
On Ch	rocto	iation							
On Cha	1	Threshold Voltage		$V_{DS} = V_{GS}, I_{D} = 250$	μ <b>Δ</b> (	1.0		2.5	V
V <sub>GS(th)</sub>		0		$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$ $V_{GS} = 10 \text{V}, I_D = 6.8 \text{A}$ $V_{GS} = 5 \text{V}, I_D = 6.8 \text{A}$				-	v
R <sub>DS(on)</sub>		Drain-Source sistance					0.088 0.110	0.11 0.14	Ω
9 <sub>FS</sub>	Forwa	rd Transconductance		$V_{\rm DS} = 25 \text{ V}, \text{ I}_{\rm D} = 6.8 \text{ J}$	A		7		S
	1	racteristics							
Ciss		Capacitance		$V_{DS}$ = 25 V, $V_{GS}$ = 0	V,		270	350	pF
C <sub>oss</sub>		t Capacitance		f = 1.0 MHz			95	125	pF
C <sub>rss</sub>	Rever	se Transfer Capacitanc	e				17	23	pF
Switch	ina Ch	aracteristics							
t <sub>d(on)</sub>		On Delay Time					8	25	ns
t <sub>r</sub>		On Rise Time		$V_{DD} = 30 \text{ V}, \text{ I}_{D} = 6.8 \text{ J}$	А,		90	190	ns
t <sub>d(off)</sub>		Off Delay Time		R <sub>G</sub> = 25 Ω			20	50	ns
t <sub>f</sub>		Off Fall Time			(Note 4)		40	90	ns
Q <sub>g</sub>		Sate Charge		V <sub>DS</sub> = 48 V, I <sub>D</sub> = 13.6	ς Δ		4.8	6.4	nC
Q <sub>gs</sub>		Source Charge		$V_{GS} = 5 V$	, n,		1.6		nC
Q <sub>ad</sub>		Drain Charge		(Note			2.7		nC
	1								1
Drain-S	Source	<b>Diode Character</b>	istics an	d Maximum Rati	ngs				
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current					13.6	Α		
I <sub>SM</sub>	Maxim	um Pulsed Drain-Sourc	ce Diode Fo	de Forward Current				54.4	Α
V <sub>SD</sub>	Drain-	Source Diode Forward	Voltage	$V_{GS}$ = 0 V, $I_{S}$ = 13.6	A			1.5	V
t <sub>rr</sub>	Rever	se Recovery Time		$V_{GS} = 0 V, I_S = 13.6 A,$ $dI_F / dt = 100 A/\mu s$			45	-	ns
Q <sub>rr</sub>	Rever	se Recovery Charge					45		nC

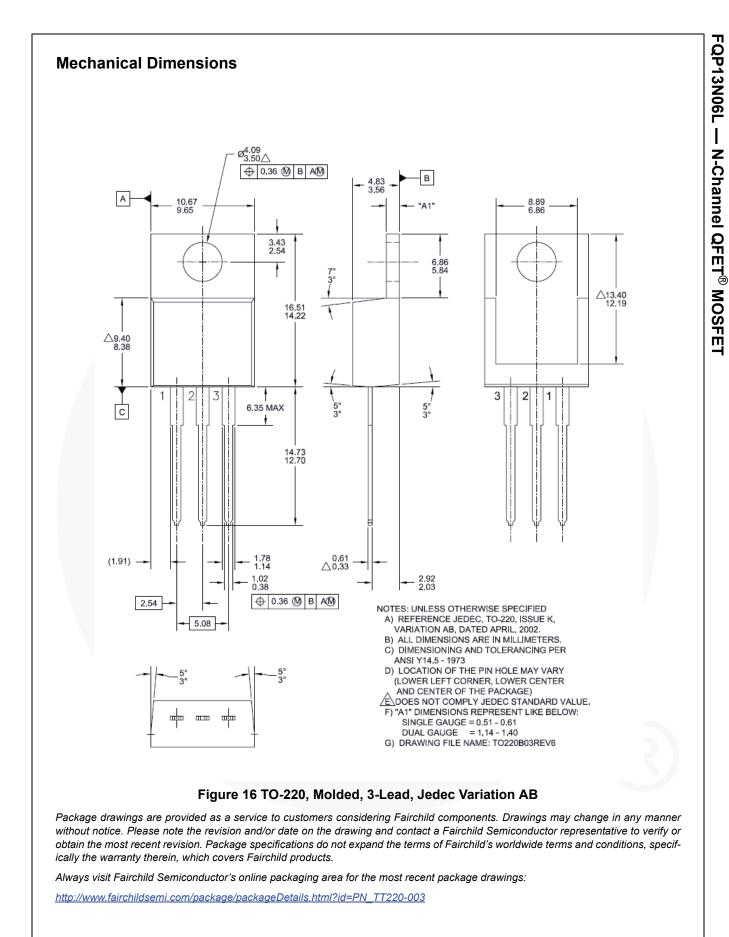
3.  $I_{SD} \le 13.6$  Å,  $di/dt \le 300$  Å/µs,  $V_{DD} \le BV_{DSS}$ , starting  $T_J = 25^{\circ}C$ . 4. Essentially independent of operating temperature. FQP13N06L — N-Channel QFET<sup>®</sup> MOSFET













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