



LCA100L Single-Pole OptoMOS® Relay

Parameter	Ratings	Units
Blocking Voltage	350	V _P
Load Current	120	mA _{rms} & mA _{DC}
On-Resistance (max)	25	Ω

Features

- 3750V_{rms} Input/Output Isolation
- Current Limiting
- Low Drive Power Requirements
- High Reliability
- · Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 6-Pin DIP Package
- Flammability Rating UL 94 V-0
- Surface Mount Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - · Hook Switch
 - Dial Pulsing
 - Ground Start
 - · Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- · Electronic Switching
- I/O Subsystems
- · Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

The LCA100L is a current-limiting, 350V, 120mA, 25Ω , 1-Form-A relay. It features one of the lowest on-resistances available in a 350V OptoMOS relay.

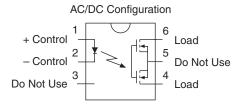
Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1172007
- EN/IEC 60950-1 Certified Component: Certificate available on our website

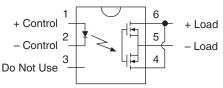
Ordering Information

Part #	Description
LCA100L	6-Pin DIP (50/Tube)
LCA100LS	6-Pin Surface Mount (50/Tube)
LCA100LSTR	6-Pin Surface Mount (1,000/Reel)

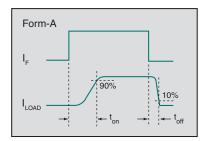
Pin Configuration



DC Only Configuration



Switching Characteristics of Normally Open (Form A) Devices











Absolute Maximum Ratings @25°C

Parameter	Ratings	Units
Blocking Voltage	350	V_{P}
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	Α
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V_{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @25°C (Unless Otherwise Noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics	'	'				
Load Current						
AC/DC Configuration, Continuous ¹	-	IL	-	-	120	mA _{rms} & mA _{DC}
DC Configuration, Continuous ¹	-	IL	-	-	200	mA _{DC}
On-Resistance ²						
AC/DC Configuration	I _L =120mA	D	-	-	25	Ω
DC Configuration	I _L =200mA	R _{ON}	-	-	8	22
Off-State Leakage Current	V _L =350V	I _{LEAK}	-	-	1	μΑ
Switching Speeds						
Turn-On	I 5 A \/ 10\/	t _{on}	-	-	5	ms
Turn-Off	$I_F = 5mA, V_L = 10V$	t _{off}	-	-	5	ms
Output Capacitance	I _F =0mA, V=50V, f=1MHz	C _{OUT}	-	35	-	pF
Load Current Limiting	-	I _{CL}	130	170	210	mA
Input Characteristics	·					
Input Control Current to Activate	I _L = 120mA	l _F	-	-	5	mA
Input Control Current to Deactivate	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F = 5mA	V_{F}	0.9	1.2	1.5	V
Reverse Input Current	$V_R = 5V$	I _R	-	-	10	μΑ
Common Characteristics					•	
Capacitance, Input to Output	V _{IO} =0V, f=1MHz	C _{IO}	-	3	-	pF

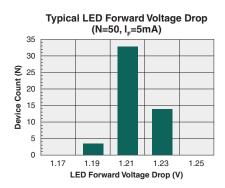
¹ If both poles operate, then the load current must be derated so that the package power dissipation value is not exceeded.

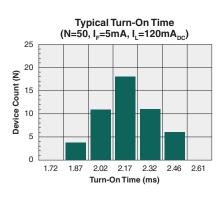
² Derate linearly 6.67 mW / °C

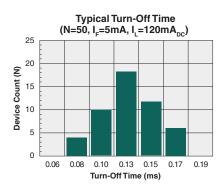
² Measurement taken within 1 second of on-time.

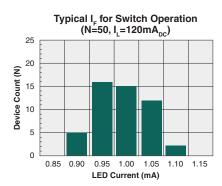


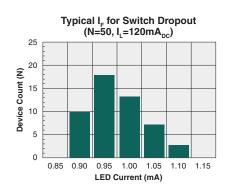
PERFORMANCE DATA*

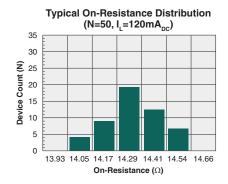


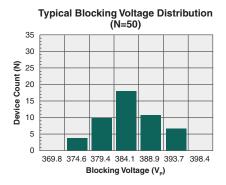


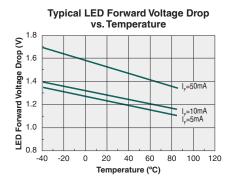


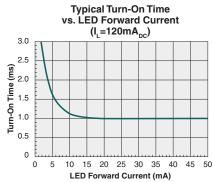


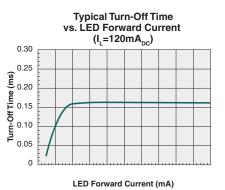








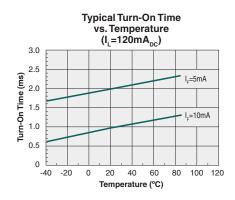


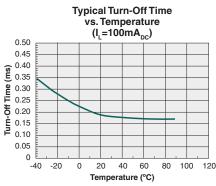


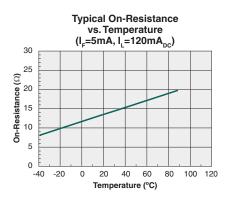
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C. For guaranteed parameters not indicated in the written specifications, please contact our application department.

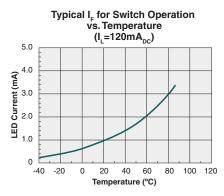


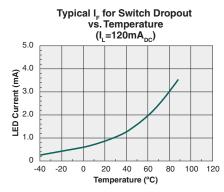
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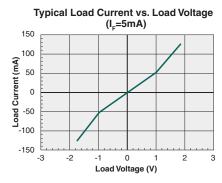


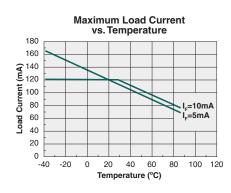


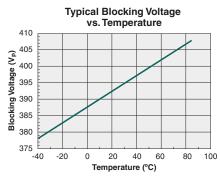


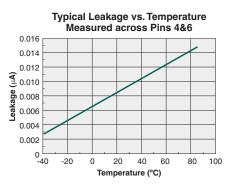


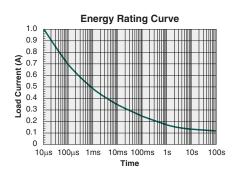


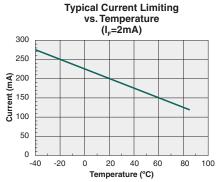












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Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LCA100L / LCA100LS	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be (T_C - 5)°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature (T _c)	Dwell Time (t _p)	Max Reflow Cycles
LCA100L	250°C	30 seconds	1
LCA100LS	250°C	30 seconds	3

Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.



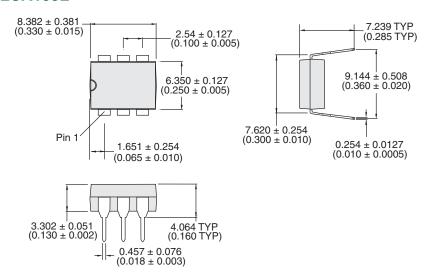




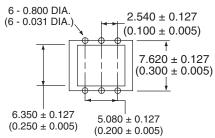


MECHANICAL DIMENSIONS

LCA₁₀₀L

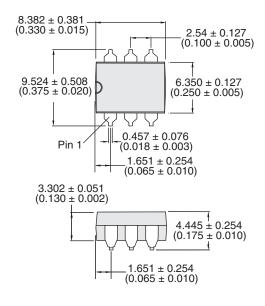


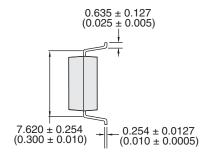
PCB Hole Pattern

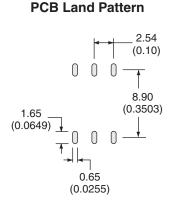


Dimensions mm (inches)

LCA100LS



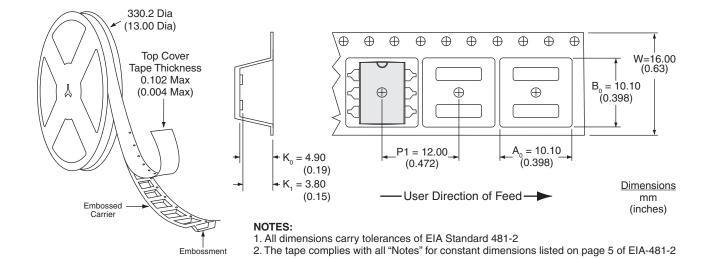




Dimensions mm (inches)



LCA100LSTR Tape & Reel



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